

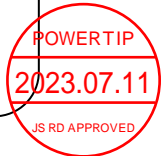


SPECIFICATIONS

CUSTOMER	:	PTC
SAMPLE CODE	:	SH102600T015-IBC01
MASS PRODUCTION CODE	:	PH102600T015-IBC01
SAMPLE VERSION	:	02
SPECIFICATIONS EDITION	:	004
DRAWING NO. (Ver.)	:	JLMD-PH102600T015-IBC01_002
PACKAGING NO. (Ver.)	:	JPKG-PH102600T015-IBC01_002

Customer Approved

Date:



Approved	Checked	Designer
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- Preliminary specification for design input
- Specification for sample approval

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History of Version

<u>Date</u> <small>(mm / dd / yyyy)</small>	<u>Ver.</u>	<u>Edi.</u>	<u>Description</u>	<u>Page</u>	<u>Design by</u>
10/15/2021	01	001	New Drawing	-	俞承澤
01/26/2022	01	002	New Sample	-	俞承澤
06/17/2022	01	003	Modify TP	10, Appendix	俞承澤
06/14/2023	02	004	Update Sample Specifications	-	王菲

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1. SPECIFICATIONS

1.1 Features

<u>Item</u>	<u>Standard Value</u>
Display Type	1024 * 3 (RGB) * 600 Dots
LCD Type	a-Si TFT , Normally white , Transmissive type
Gray Scale Inversion Direction	6 o'clock
Eyes Viewing Direction	12 o'clock
Screen size(inch)	7.0 inch
Color configuration	RGB-Strip
Interface	LVDS Interface
Other(controller/driver IC)	EK79001EB+EK73215BCGB (Or Compatible IC)
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Outline Dimension	192.96(W) * 121.4(L) * 4.6 (H)	mm

LCD panel

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Viewing Area	155.05(W) * 86.72 (L)	mm
Active Area	154.21 (W) * 85.92 (L)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Voltage	V _{DD}	-	-0.5	5	V
	A _{VDD}	-	-0.5	15	V
	V _{GH}	-	-0.3	40	V
	V _{GL}	-	-20	0.3	V
Operating Temperature	T _{OP} (T _s)	Note 1	-20	70	°C
Storage Temperature	T _{ST} (T _a)	Note 2	-30	80	°C

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1 : T_s is the temperature of panel's surface.

Note 2 : T_a is the ambient temperature of samples.

1.4 DC Electrical Characteristics

GND = 0V, T_a = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Power Supply	V _{DD}	-	2.5	3.3	3.6	V	-
	A _{VDD}	-	9.4	9.6	9.8	V	-
	V _{GH}	-	17.0	18.0	19.0	V	-
	V _{GL}	-	-7.0	-6.0	-5.0	V	-
Input signal Voltage	V _{COM}	-	3.0	3.6	4.2	V	Note2
Input Signal Voltage	V _{IH}	-	0.7V _{DD}	-	V _{DD}	V	-
	V _{IL}	-	0	-	0.3 V _{DD}	V	
Supply Current	I _{DD}	V _{DD} = 3.3 V Pattern= Red *1	-	25	40	mA	Note1
	I _{ADD}	A _{VDD} =9.6V Pattern= Red	-	15	25	mA	
	I _{GH}	V _{GH} =18.0V Pattern= Red	-	0.5	1.0	mA	
	I _{GL}	V _{GL} =-6.0V Pattern= Red	-	1.0	1.5	mA	

Note1:Maximum current display

Note2: V_{COM} must be adjusted to optimize display quality: cross-talk, contrast ratio and etc

1.5 Optical Characteristics

TFT LCD Module

VDD = 3.3 V, Ta=25°C

Item		Symbol	Condition	Min.	Typ.	Max.	unit	
Response time	Tr + Tf		Ta = 25°C θX, θY = 0°	-	30	45	ms	Note 2
Viewing angle	Top	θY+	CR ≥ 10	-	60	-	Deg.	Note 4
	Bottom	θY-		-	60	-		
	Left	θX-		-	60	-		
	Right	θX+		-	60	-		
Contrast ratio		CR	-	500	600	-	-	Note 3
Color of CIE Coordinate	White	X	IF=200mA	0.25	0.30	0.35	-	Note 1
		Y		0.30	0.35	0.40		
	Red	X		0.54	0.59	0.64		
		Y		0.28	0.33	0.38		
	Green	X		0.30	0.35	0.40		
		Y		0.56	0.61	0.66		
	Blue	X		0.11	0.16	0.21		
		Y		0.07	0.12	0.17		
Average Brightness Pattern=white display (With LCD &TP)		IV	IF=200mA	350	470	-	cd/m ²	Note 1
Uniformity (With LCD &TP)		△B	IF=200mA	70	-	-	%	Note 1

Note 1:

*1 : $\Delta B = B(\text{min}) / B(\text{max}) * 100\%$

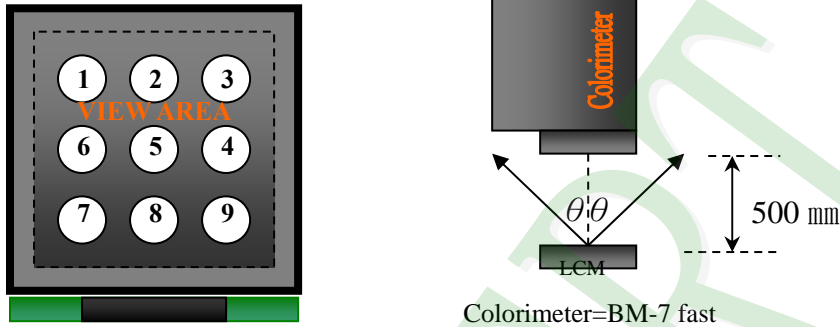
*2 : Measurement Condition for Optical Characteristics:

a : Environment: $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ / $60 \pm 20\%$ R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: 500 ± 50 mm , ($\theta = 0^{\circ}$)

c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.

d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

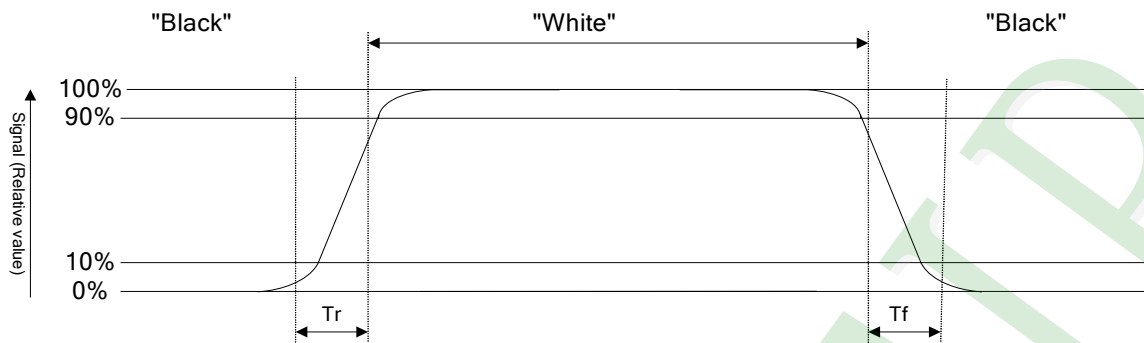
The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White



Normally Black



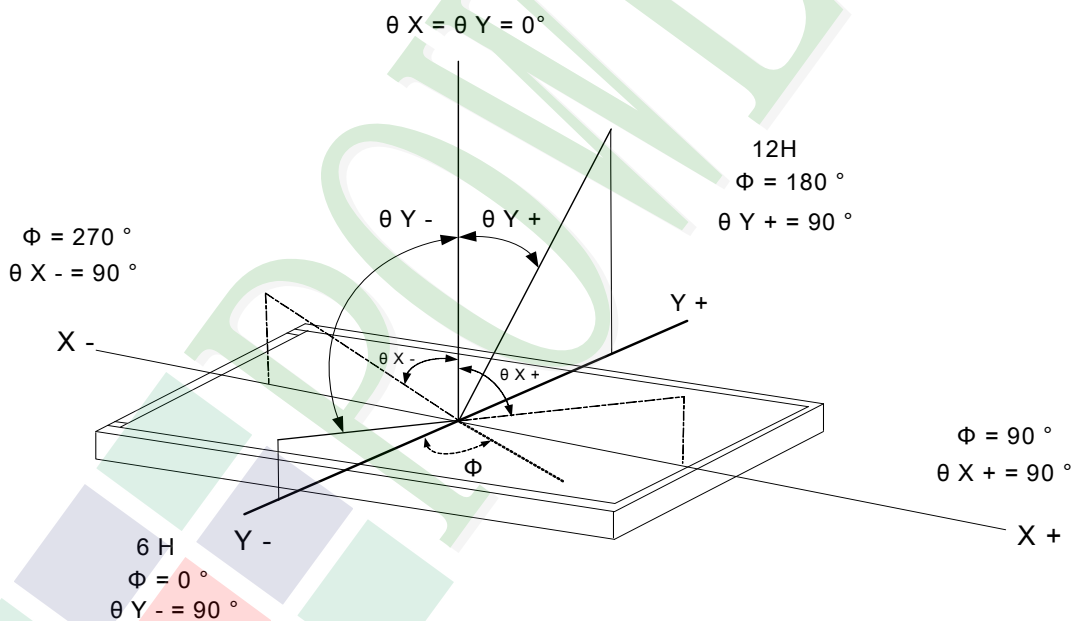
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



Note5: Applying with spectrophotometer in the condition of 400 to 700nm, 10nm/each; in accordance with JIS Z 8701 2 degree viewing XYZ system, measuring the reflective rate of 5 degree

1.6 Backlight Unit Characteristics

Maximum Ratings

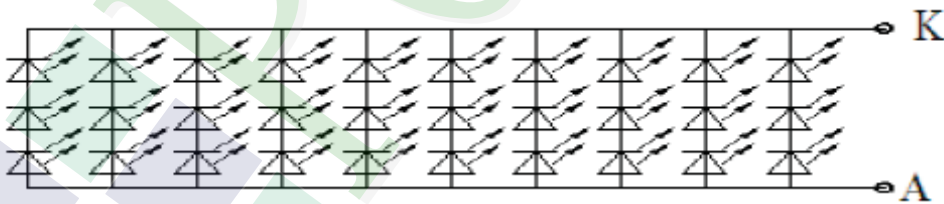
<u>Item</u>	<u>Symbol</u>	<u>Conditions</u>	<u>Min.</u>	<u>Max.</u>	<u>Unit</u>
LED Forward Current	IF	Ta =25°C	-	300	mA
LED Reverse Voltage	VR	Ta =25°C	-	5	V
Power Dissipation	PD	Ta =25°C	-	1.98	W

Backlight Characteristics

<u>Item</u>	<u>Symbol</u>	<u>Conditions</u>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>Unit</u>
Forward Voltage	VF	IF=200mA	8.1	9.3	10.2	V
Average Brightness (Without LCD &TP)	IV		11200	12800	-	cd/m ²
CIE Color Coordinate (Without LCD &TP)	X		0.25	0.28	0.31	-
	Y		0.28	0.31	0.34	
Uniformity *1	△B		75	-	-	*2
Color		White				

*1 : This value will be changed while mass production.

*2 : $\Delta B = B(\text{min}) / B(\text{max})\%$
B/L Internal Circuit Diagram



Other Description

<u>Item</u>	<u>Conditions</u>	<u>Description</u>
Life Time	Ta =25°C IF= 200 mA	20000 hrs

1.7 Touch Panel Unit Characteristics

Features

<u>Item</u>	<u>Standard Value</u>
Touch Panel Size	7.0"
Touch type	Capacitive Touch Panel
Input Method	Finger Or Conductive Pen
Output Interface	I ² C
IC	ICNT8952

I2C Address

<u>Bit 7</u>	<u>Bit 6</u>	<u>Bit 5</u>	<u>Bit 4</u>	<u>Bit 3</u>	<u>Bit 2</u>	<u>Bit 1</u>	<u>Bit 0</u>
0	1	1	1	0	0	0	R/W

Bit 0: 0 for Write / 1 for Read

DC Electrical Characteristics

<u>Item</u>	<u>Symbol</u>	<u>Condition</u>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>unit</u>
Supply Voltage	TPVDD	-	-	3.3	-	V

Optical Characteristics

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Total light transmittance	85% or more	-

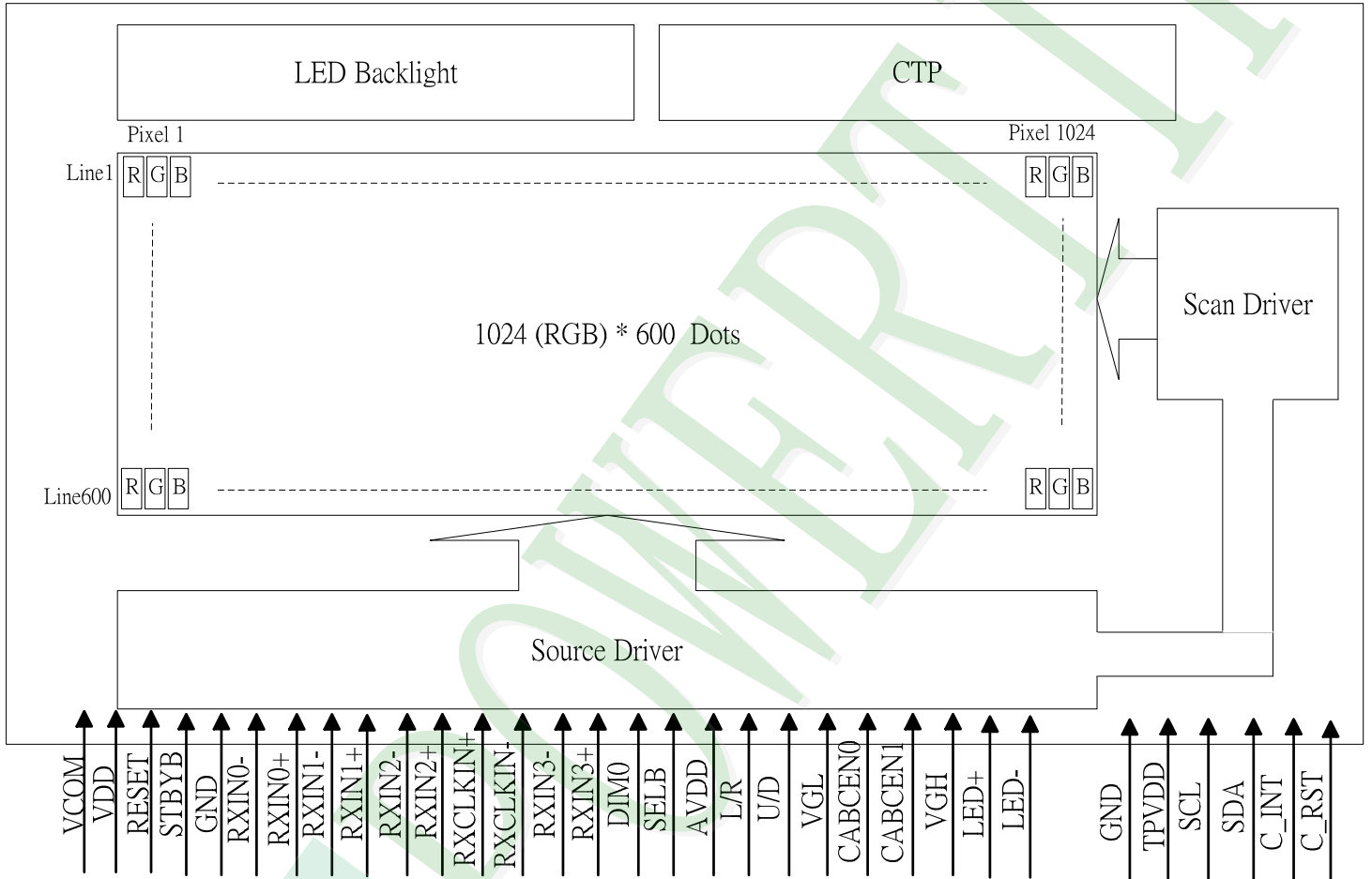
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



2.2 Interface Pin Description

Pin No.	Symbol	Function
1	VCOM	Common Voltage
2	VDD	Power Voltage for digital circuit
3	VDD	Power Voltage for digital circuit
4	NC	No connection
5	Reset	Global reset pin
6	STBYB	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z
7	GND	Ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	+ LVDS differential data input
10	GND	Ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	+ LVDS differential data input
13	GND	Ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	+ LVDS differential data input
16	GND	Ground
17	RXCLKIN-	- LVDS differential clock input
18	RXCLKIN+	+ LVDS differential clock input
19	GND	Ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	+ LVDS differential data input
22	GND	Ground
23	NC	No Connection
24	NC	No Connection
25	GND	Ground

<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
26	NC	No Connection
27	DIM0	Backlight CABC controller signal output DIM0=L Turn off external backlight controller DIM0=H Logical control signal to turn on external backlight controller
28	SELB	6bit/8bit mode select If LVDS input data is 6 bits ,SELB must be set to High; If LVDS input data is 8 bits ,SELB must be set to Low.
29	AVDD	Power for Analog Circuit
30	GND	Ground
31	LEDK	LED Cathode
32	LEDK	LED Cathode
33	L/R	Horizontal inversion When L/R="0", set right to left scan direction. When L/R="1", set left to right scan direction.
34	U/D	Vertical inversion When U/D="0", set top to bottom scan direction. When U/D="1", set bottom to top scan direction.
35	VGL	Gate OFF Voltage
36	CABCEN1	CABC H/W enable Note:1
37	CABCEN0	CABC H/W enable Note:1
38	VGH	Gate ON Voltage
39	LEDA	LED Anode
40	LEDA	LED Anode

Note1:

<u>CABCEN1</u>	<u>CABCEN0</u>	<u>DESCRIPTION</u>
L	L	CABC OFF
L	H	User interface Image
H	L	Still Picture
H	H	Moving Image

Capacitive Touch Panel (CTP) Interface

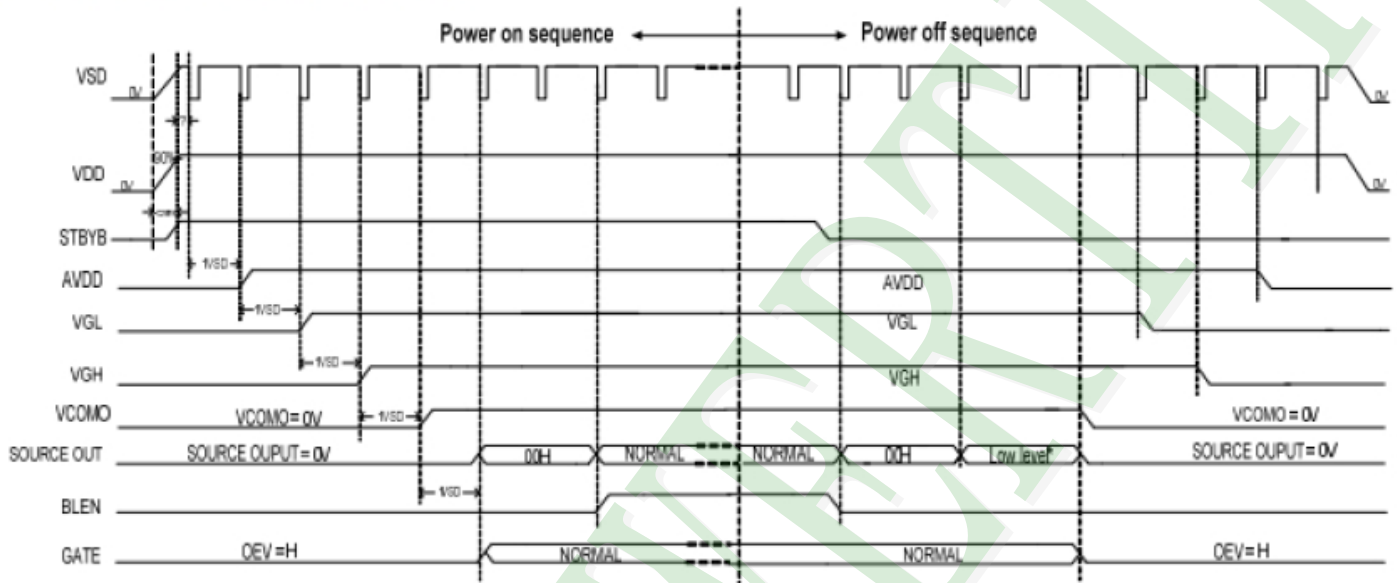
<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
1	GND	Ground.
2	TPVDD	Power.
3	SCL	I ² C Clock.
4	SDA	I ² C Data.
5	INT	The interrupt from the CTP to the Host H: CTP interrupt not requested L: CTP request interrupt
6	RESET	RESET.

2.3 Timing Characteristics

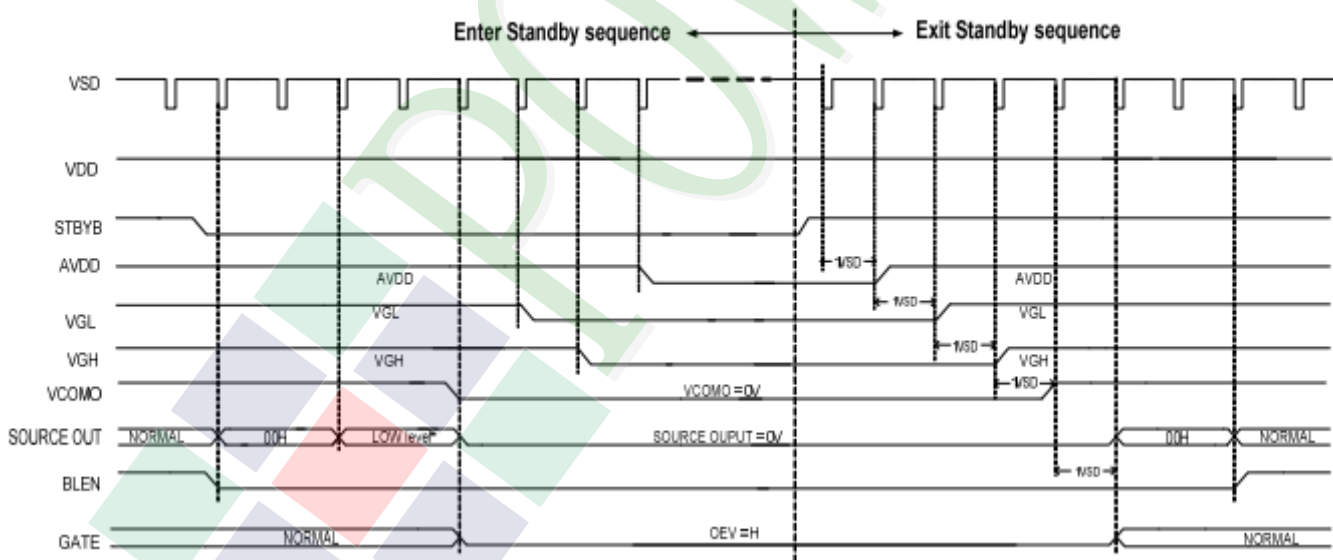
2.3.1 Power Sequence

In order to prevent IC from power on reset fail, the rising time (T_{POR}) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

Power-On/Off Timing Sequence:

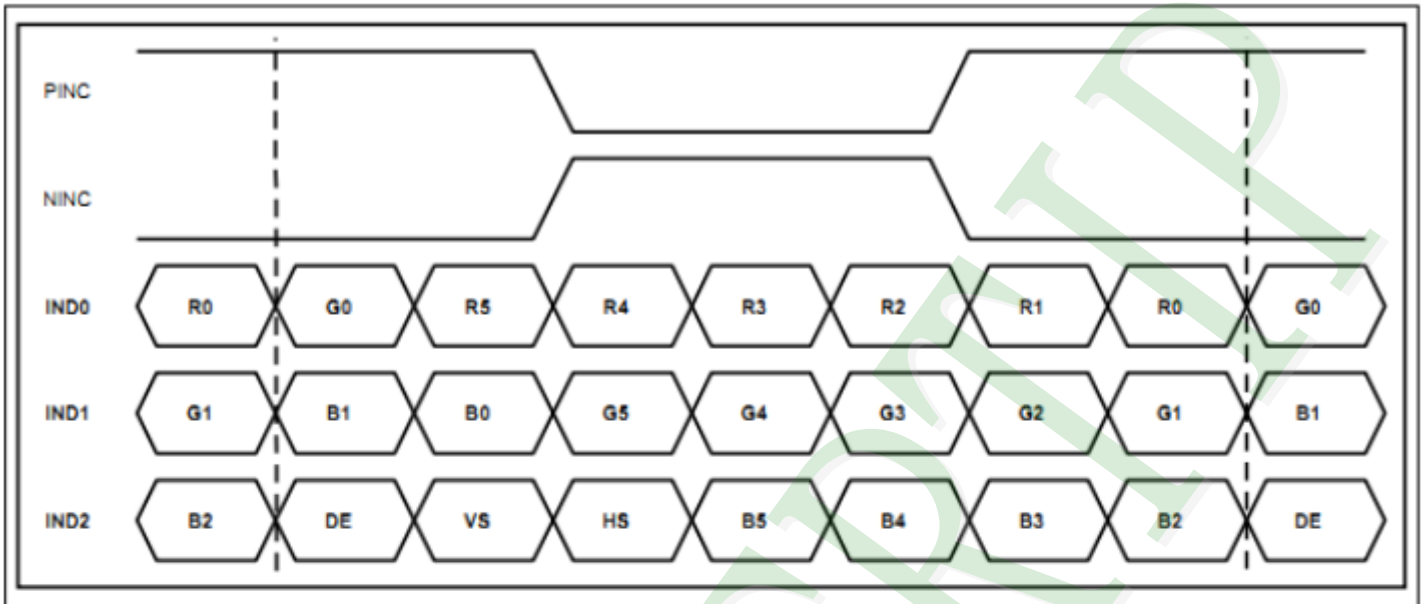


Enter and Exit Standby Mode Sequence:



2.3.2 Data Input Format for LVDS

6bit LVDS input



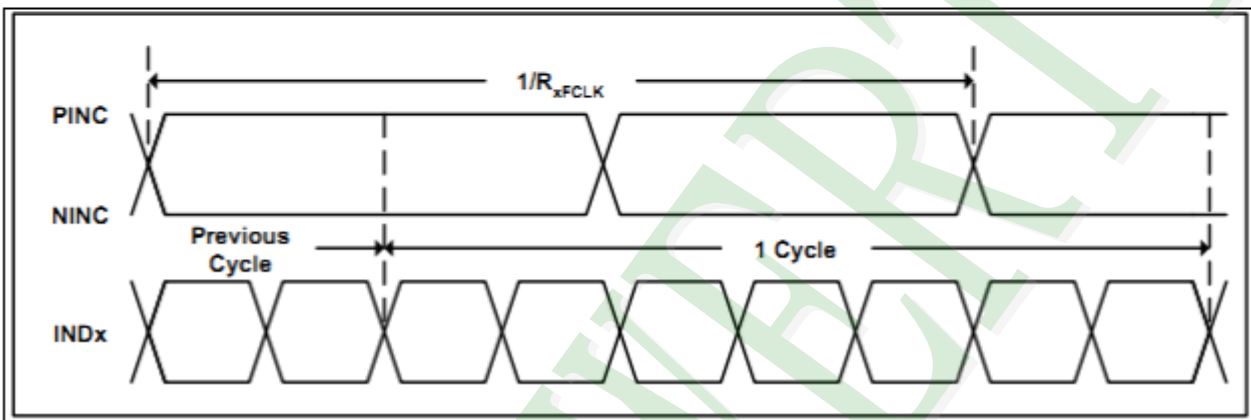
8-bit LVDS input



2.4 AC Electrical Characteristics

LVDS mode

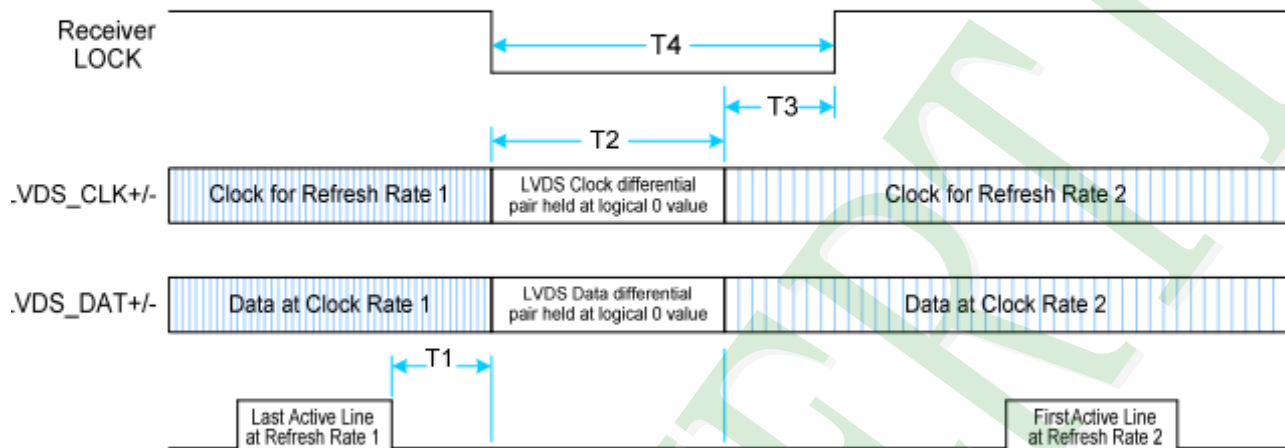
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditio
Clock frequency	R_{XFCLK}	20		71	MHz	
Input data skew margin	T_{RSKM}	500			pS	$ V_{ID} = 400\text{mV}$ $R_{XVCM} = 1.2\text{V}$ $R_{XFCLK} = 71\text{MHz}$
Clock high time	T_{LVCH}		$4/(7 * R_{XFCLK})$		ns	
Clock low time	T_{LVCL}		$3/(7 * R_{XFCLK})$		ns	
PLL wake-up time	T_{ENPLL}			150	uS	



2.5 Timing

SDRRS (seamless display refresh rate switching)

When Showing the still picture, it is accept to reduce the refresh rate from 60Hz to low refresh rate (for example 40Hz). The purpose is mainly for power saving. INTEL defined a timing chart switch between different refresh rate. Following this timing chart, the switch between different refresh rates is seamless for end user.



T1 - Min delay from start of vert blank to start of timing change: 2 lines (HSYNC periods)

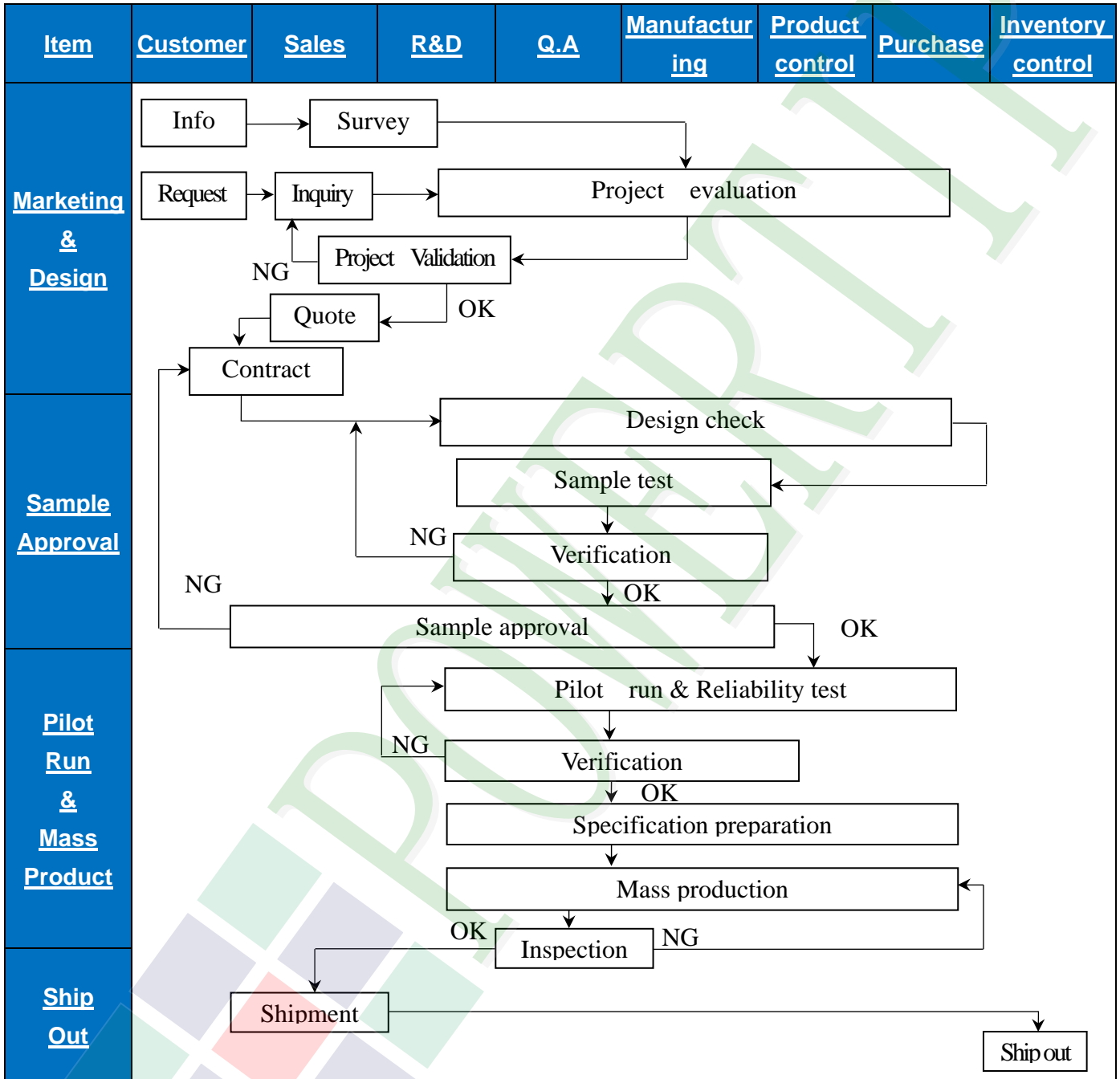
T2 - Max delay for clock to transition to new frequency: 100us

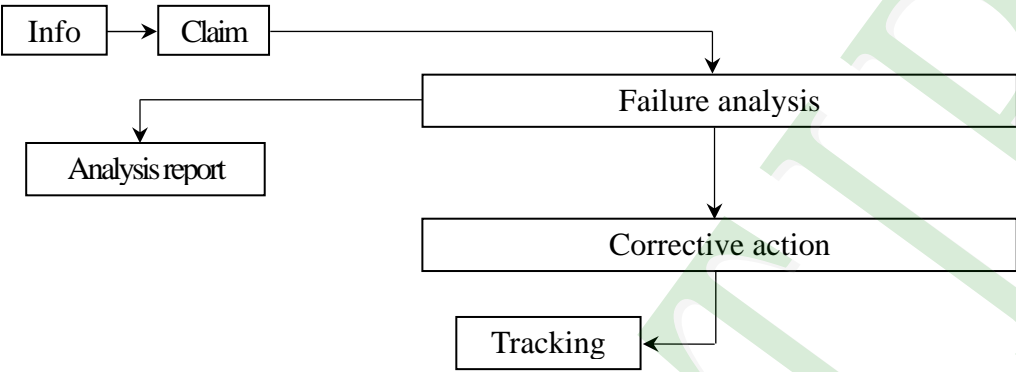
T3 - Max receiver lock delay from stable clock: Display specific (TBD)

T4 - Max period during which panel maintains display (T2+T3): Display specific (TBD)

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



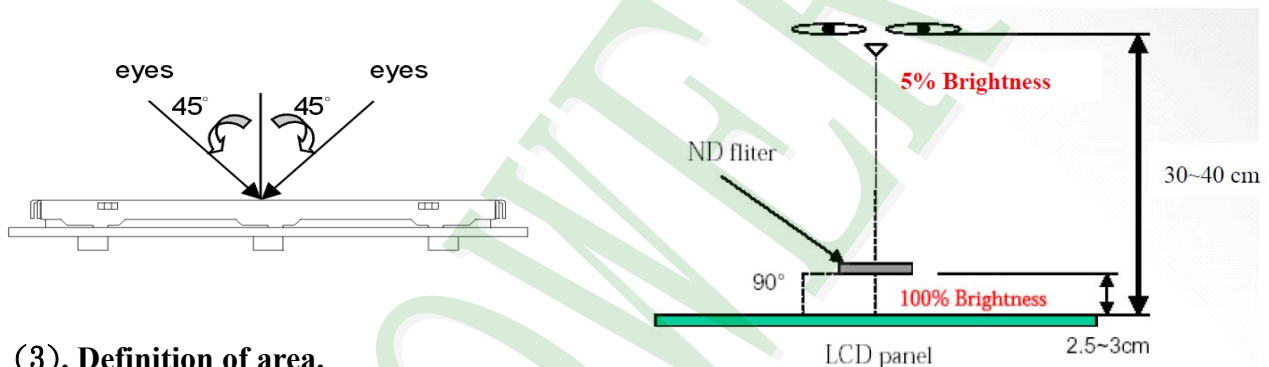
Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD Info[Info] --> Claim[Claim] Claim --> Failure[Failure analysis] Failure --> Report[Analysis report] Failure --> Action[Corrective action] Action --> Tracking[Tracking] </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

3.2. Inspection Specification

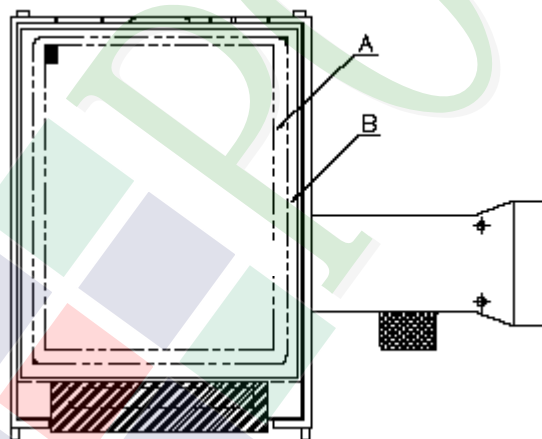
- ◆Scope: The document shall be applied to TFT-LCD Module for 3.5" -15" (Ver.B01).
- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.
- ◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample
- ◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5
- ◆OUT Going Defect Level: Sampling.
- ◆Standard of the product appearance test:

a. Manner of appearance test:

- (1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)
, and distance of view must be at 30~40 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



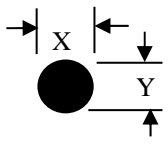
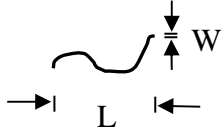
A area: viewing area

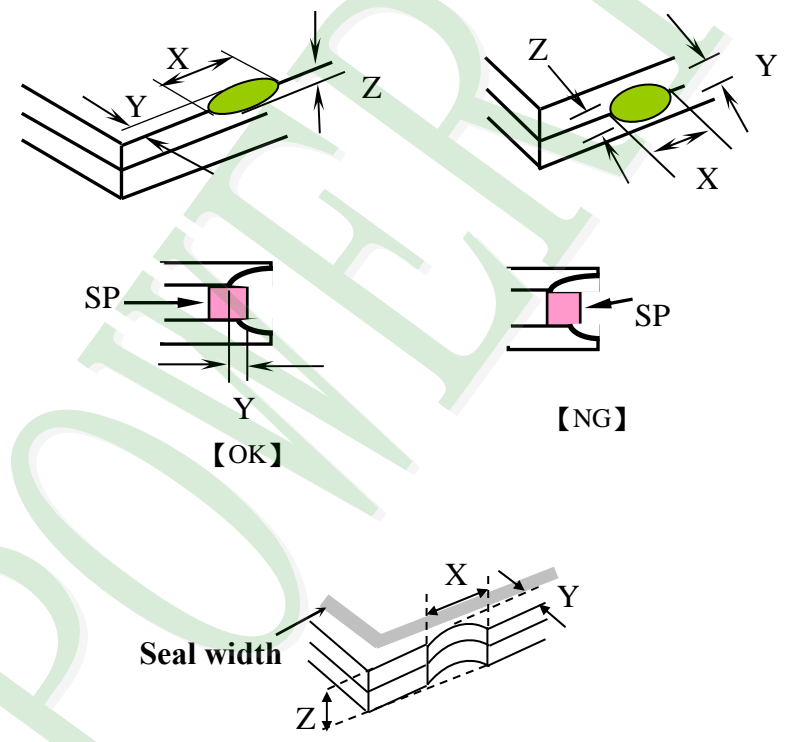
B area: Outside of viewing area

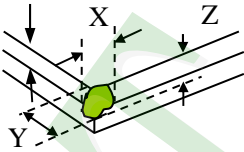
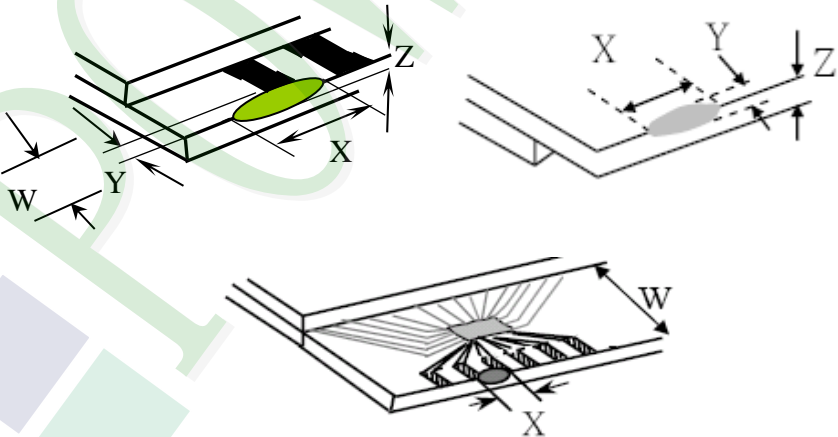
(4). Standard of inspection : (Unit : mm)

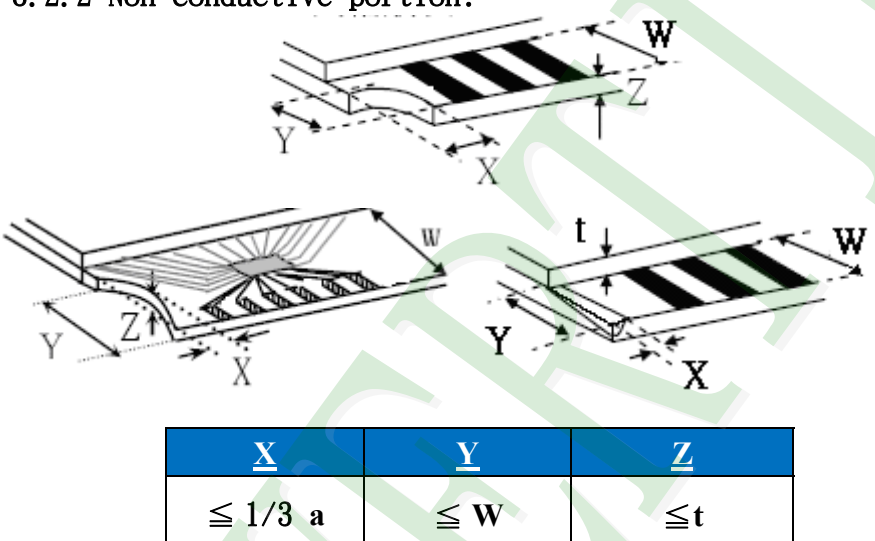
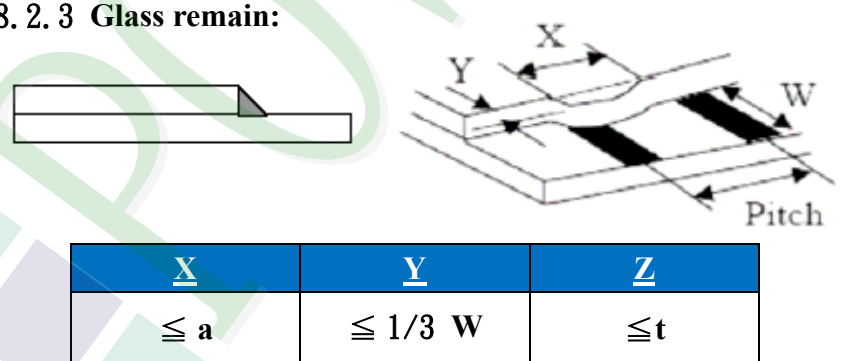
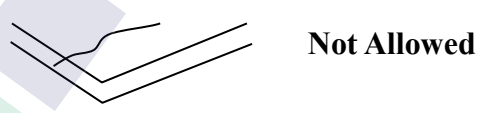
◆Specification For TFT-LCD Module 3.5" ~15" :
(Ver.B01)

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	<u>Level</u>										
01	Product condition	1. 1The part number is inconsistent with work order of production.	Major										
		1. 2 Mixed product types.	Major										
		1. 3 Assembled in inverse direction.	Major										
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major										
03	Outline dimension	3. 1Product dimension and structure must conform to structure diagram.	Major										
04	Electrical Testing	4. 1 Missing line character and icon.	Major										
		4. 2 No function or no display.	Major										
		4. 3 Display malfunction.	Major										
		4. 4 LCD viewing angle defect.	Major										
		4. 5 Current consumption exceeds product specifications.	Major										
		4. 6Mura cannot be seen through 5% ND filter at 50% Gray , should be judged by the viewing angle of 90 degree.	Minor										
05	Dot defect (Bright dot, Dark dot) On -display	<table border="1" data-bbox="561 1167 1273 1442"> <thead> <tr> <th><u>Item</u></th> <th><u>Acceptance (Q'ty)</u></th> </tr> </thead> <tbody> <tr> <td>Bright Dot</td> <td>≤ 4</td> </tr> <tr> <td>Dark Dot</td> <td>≤ 5</td> </tr> <tr> <td>Joint Dot</td> <td>≤ 3</td> </tr> <tr> <td>Total</td> <td>≤ 7</td> </tr> </tbody> </table>	<u>Item</u>	<u>Acceptance (Q'ty)</u>	Bright Dot	≤ 4	Dark Dot	≤ 5	Joint Dot	≤ 3	Total	≤ 7	Minor
		<u>Item</u>	<u>Acceptance (Q'ty)</u>										
Bright Dot	≤ 4												
Dark Dot	≤ 5												
Joint Dot	≤ 3												
Total	≤ 7												
5.1 Inspection pattern: full white, full black, Red, Green and blue screens. 5.2 It is defined as dot defect if defect area > 1/2 dot. 5.3 The distance between two dot defect ≥ 5 mm. 5.4 Bright dot : Dots appear bright and unchanged in visible with 5% ND filter is defined. 5.5 Tiny bright dot: bright dot area ≤ 1/2 dot. a. Dots appear bright and unchanged in visible with 5% ND filter is defined defect and is judged in accordance with 6.1 b. Dots invisible with 5% ND Filter is Ignored.													

NO	Item	Criterion	Level																																																					
06	<p>Black or white Dot, scratch, contamination</p> <p>Round type</p>  <p>$\Phi = (x + y) / 2$</p> <p>Line type</p> 	<p>6. 1 Round type (Non-display or display):</p> <table border="1" data-bbox="512 421 1289 698"> <thead> <tr> <th rowspan="2">Dimension (diameter : Φ)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.25$</td> <td>Ignore</td> <td rowspan="3">Ignore</td> </tr> <tr> <td>$0.25 < \Phi \leq 0.50$</td> <td>5</td> </tr> <tr> <td>$\Phi > 0.50$</td> <td>0</td> </tr> <tr> <td>Total</td> <td>5</td> <td></td> </tr> </tbody> </table> <p>6. 2 Line type(Non-display or display):</p> <table border="1" data-bbox="432 817 1369 1361"> <thead> <tr> <th rowspan="2">module size</th> <th rowspan="2">Length (L)</th> <th rowspan="2">Width (W)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td rowspan="5">3.5" to less 9"</td> <td>---</td> <td>$W \leq 0.03$</td> <td>Ignore</td> <td rowspan="5">Ignore</td> </tr> <tr> <td>$L \leq 10.0$</td> <td>$0.03 < W \leq 0.05$</td> <td>4</td> </tr> <tr> <td>$L \leq 5.0$</td> <td>$0.05 < W \leq 0.10$</td> <td>2</td> </tr> <tr> <td>---</td> <td>$W > 0.10$</td> <td>As round type</td> </tr> <tr> <td>Total</td> <td></td> <td>5</td> </tr> <tr> <td rowspan="4">9" to 15"</td> <td>---</td> <td>$W \leq 0.05$</td> <td>Ignore</td> <td rowspan="4">Ignore</td> </tr> <tr> <td>$L \leq 10.0$</td> <td>$0.05 < W \leq 0.10$</td> <td>5</td> </tr> <tr> <td>---</td> <td>$W > 0.10$</td> <td>As round type</td> </tr> <tr> <td>Total</td> <td></td> <td>5</td> </tr> </tbody> </table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.25$	Ignore	Ignore	$0.25 < \Phi \leq 0.50$	5	$\Phi > 0.50$	0	Total	5		module size	Length (L)	Width (W)	Acceptance (Q'ty)		A area	B area	3.5" to less 9"	---	$W \leq 0.03$	Ignore	Ignore	$L \leq 10.0$	$0.03 < W \leq 0.05$	4	$L \leq 5.0$	$0.05 < W \leq 0.10$	2	---	$W > 0.10$	As round type	Total		5	9" to 15"	---	$W \leq 0.05$	Ignore	Ignore	$L \leq 10.0$	$0.05 < W \leq 0.10$	5	---	$W > 0.10$	As round type	Total		5	Minor
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07	<p>Polarizer Bubble</p>	<table border="1" data-bbox="480 1503 1321 1921"> <thead> <tr> <th rowspan="2">Dimension (diameter: Φ)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.25$</td> <td>Ignore</td> <td rowspan="5">Ignore</td> </tr> <tr> <td>$0.25 < \Phi \leq 0.50$</td> <td>4</td> </tr> <tr> <td>$0.50 < \Phi \leq 0.80$</td> <td>1</td> </tr> <tr> <td>$\Phi > 0.80$</td> <td>0</td> </tr> <tr> <td>Total</td> <td>5</td> </tr> </tbody> </table>	Dimension (diameter: Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.25$	Ignore	Ignore	$0.25 < \Phi \leq 0.50$	4	$0.50 < \Phi \leq 0.80$	1	$\Phi > 0.80$	0	Total	5	Minor																																					
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<p>8.2 Protrusion over terminal:</p> <p>8.2.1 Chip on electrode pad:</p>  <table border="1" data-bbox="560 1697 1347 1872"> <thead> <tr> <th></th> <th><u>X</u></th> <th><u>Y</u></th> <th><u>Z</u></th> </tr> </thead> <tbody> <tr> <td>Front</td> <td>$\leq a$</td> <td>$\leq 1/2 W$</td> <td>$\leq t$</td> </tr> <tr> <td>Back</td> <td>$\leq a$</td> <td>$\leq W$</td> <td>$\leq 1/2 t$</td> </tr> </tbody> </table>		<u>X</u>	<u>Y</u>	<u>Z</u>	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	$\leq a$	$\leq W$	$\leq 1/2 t$	Minor
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◆ Specification For TFT-LCD Module 3.5" ~15" :
(Ver.B01)

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	<u>Level</u>
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General appearance	10. 1 Pin type 、 quantity 、 dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10. 3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION										
1	High Temperature Storage Test	Keep in +80 ±5°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.										
2	Low Temperature Storage Test	Keep in -30 ±5°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.										
3	High Temperature / High Humidity Storage Test	Keep in +60 °C / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)										
4	Temperature Cycling Storage Test	<p style="text-align: center;"> $-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ (30mins) (5mins) (30mins) (5mins) ← 20 Cycle → </p> <p>Surrounding temperature, then storage at normal condition 4hrs.</p>										
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/-										
		Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/-										
		<ol style="list-style-type: none"> Temperature ambience : 15°C ~ 35°C Humidity relative : 30% ~ 60% Energy Storage Capacitance(Cs+Cd) : 150pF±10% Discharge Resistance(Rd) : 330Ω±10% Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)										
6	Vibration Test (Packaged)	<ol style="list-style-type: none"> Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration : 1.5 mm Each direction (X、Y、Z) duration for 2 Hrs 										
7	Drop Test (Packaged)	<table border="1"> <thead> <tr> <th>Packing Weight (Kg)</th> <th>Drop Height (cm)</th> </tr> </thead> <tbody> <tr> <td>0 ~ 45.4</td> <td>122</td> </tr> <tr> <td>45.4 ~ 90.8</td> <td>76</td> </tr> <tr> <td>90.8 ~ 454</td> <td>61</td> </tr> <tr> <td>Over 454</td> <td>46</td> </tr> </tbody> </table>	Packing Weight (Kg)	Drop Height (cm)	0 ~ 45.4	122	45.4 ~ 90.8	76	90.8 ~ 454	61	Over 454	46
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0 ~ 45.4	122											
45.4 ~ 90.8	76											
90.8 ~ 454	61											
Over 454	46											
		Drop Direction : ※1 corner / 3 edges / 6 sides each 1time										

◎Inspection conditions after test:

Temperature : +20~30°C

Humidity : 50~70%

Atmospheric pressure : 86~106Kpa

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

1. 包裝材料規格表 (Packaging Material) : (per carton)

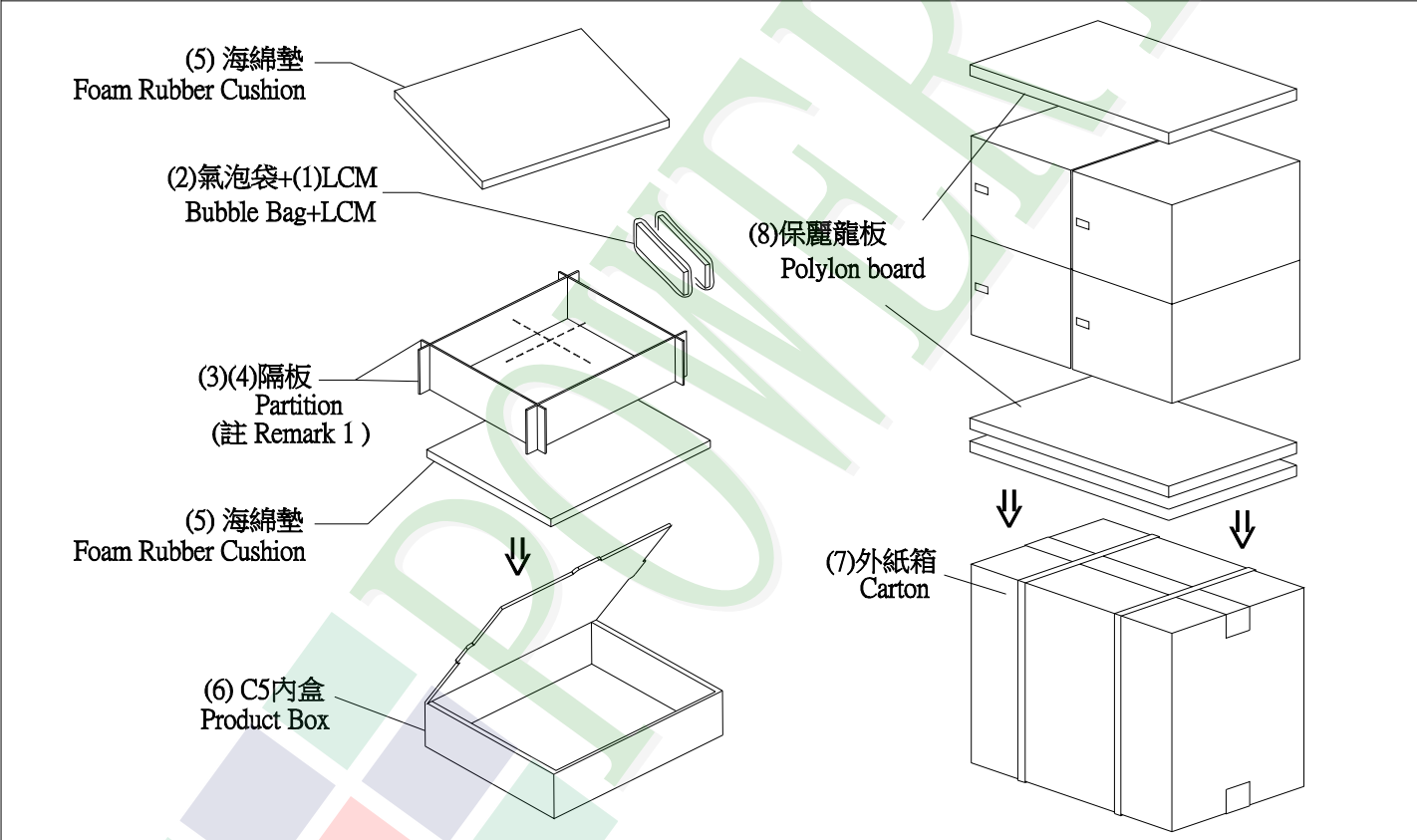
No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 ((1)LCM)	PH102600T015-IBC01	192.96X110.76X4.74	0.1623	60	9.738
2	氣泡袋(2)Bubble Bag	BAG200160BRABA	200 X 160	0.0045	60	0.27
3	A9隔板(3)A9 Partition	BX00000000058	245 X 125 X 4	0.0204	64	1.3056
4	B9隔板(4)B9 Partition	BX00000000057	295 X 125 X 4	0.0209	8	0.1672
5	海綿墊(5)Foam Rubber Cushion	OTFOAM00006ABA	290 X 240 X 10	0.02	8	0.16
6	C5內盒(6)Product Box	BX00000000059	310 X 255 X 155	0.248	4	0.992
7	外紙箱(7)Carton	BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
8	保麗龍板(8)Polylon board	OTPLB00000017	510 X 310 X 15	0.025	3	0.075
9						

2. 一整箱總重量 (Total LCD Weight in carton) : 13.54 Kg±10%

3. 單箱數量規格表 (Packaging Specifications and Quantity) :

(1)Quantity Of Spacer : A9隔板 X 16 , B9隔板 X 2

(2)Total LCM quantity in carton : quantity per box 15 x no of boxes 4 = 60



特 記 事 項 (REMARK)

4. LCM排放示意圖(前後間隔不放置):
 4. LCM placed as figure showing:
 (First and last slot should be empty)

模組(LCM) X 1pcs.