POWERTIP TECH. CORP.

SPECIFICATIONS

CUSTOMER	:	
SAMPLE CODE	:	SH102600T015-IBA
MASS PRODUCTION CODE	:	PH102600T015-IBA
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	003
DRAWING NO. (Ver.)	:	LMD-PH102600T015-IBA (Ver.003)
PACKAGING NO. (Ver.)	:	PKG-PH102600T015-IBA (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
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POWERTIP
2020.07.09
TW RD APR

☐ Preliminary specification for design input

■ Specification for sample approval

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History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
01/21/2020	01	001	New Drawing	-	Yuan
05/06/2020	01	002	First Sample Modify FPC design Modify dimension	- Appendix Appendix	Yuan
07/09/2020	01	003	Modify High Temperature Storage Test condition	28	Yuan

Contents

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	1024 * 3 (RGB) * 600 Dots
LCD Type	a-Si TFT , Normally white , Transmissive type
Gray Scale Inversion Direction	6 o'clock
Eyes Viewing Direction	12 o'clock
Screen size(inch)	7.0 inch
Color configuration	RGB-Strip
Backlight Type	LED B/L
Interface	LVDS Interface
Other(controller/driver IC)	EK79001EB+EK73215BCGB (Or Compatible IC)
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	165.75(W) * 105.4(L) * 2.65 (H)	mm

LCD panel

Item	Standard Value	Unit
Viewing Area	155.21(W) * 86.92 (L)	mm
Active Area	154.21 (W) * 85.92 (L)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
Power Voltage	V_{DD}	-	-0.5	5	V
	AV_{DD}	-	-0.5	15	V
	VGH	-	-0.3	40	V
	VGL	-	-20	0.3	V
Operating Temperature	T_{OP} (Ts)	Note 1	-20	70	°C
Storage Temperature	T_{ST} (Ta)	Note 2	-30	80	°C

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1：Ts is the temperature of panel's surface.

Note 2：Ta is the ambient temperature of samples.

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
	V_{DD}	-	2.5	3.3	3.6	V	
	AV_{DD}	-	9.4	9.6	9.8	V	
	VGH	-	17.0	18.0	19.0	V	
	VGL	-	-7.0	-6.0	-5.0	V	
Input signal Voltage	VCOM	-	3.0	3.6	4.2	V	Note2
Input Signal Voltage	VIH	-	0.7V_{DD}	-	V_{DD}	V	
	VIL	-	0	-	0.3 V_{DD}	V	
Supply Current	I_{DD}	V_{DD} = 3.3 V Pattern= Red *1	-	25	40	mA	Note1
	I_{ADD}	AV_{DD}=9.6V Pattern= Red	-	15	25	mA	
	I_{GH}	V_{GH}=18.0V Pattern= Red	-	0.5	1	mA	
	I_{GL}	VGL=-6.0V Pattern= Red	-	0.5	1	mA	

Note1:Maximum current display

Note2: VCOM must be adjusted to optimize display quality: cross-talk, contrast ratio and etc

1.5 Optical Characteristics

TFT LCD Module VDD = 3.3 V, Ta=25°C

Item		Symbol	Condition	Min.	Typ.	Max.	unit	
Response time	Tr + Tf		Ta = 25°C θX, θY = 0°	-	30	45	ms	Note 2
Viewing angle	Top	θY+	CR ≥ 10	-	60	-	Deg.	Note 4
	Bottom	θY-		-	60	-		
	Left	θX-		-	60	-		
	Right	θX+		-	60	-		
Contrast ratio		CR	Ta = 25°C θX , θY = 0°	500	600	-	-	Note 3
Color of CIE Coordinate (With B/L)	White	X		0.23	0.28	0.33	-	Note1
		Y		0.30	0.35	0.40		
	Red	X		0.53	0.58	0.63		
		Y		0.29	0.34	0.39		
	Green	X		0.29	0.34	0.39		
		Y		0.55	0.60	0.65		
	Blue	X		0.09	0.14	0.19		
		Y		0.06	0.11	0.16		
Average Brightness Pattern=white display (With LCD)*1		IV	IF=200mA	400	500	-	cd/m^2	Note1
Uniformity (With LCD)*2		△B	IF=200mA	70	-	-	%	Note1

Note 1:

*1：△B=B(min) / B(max) * 100%

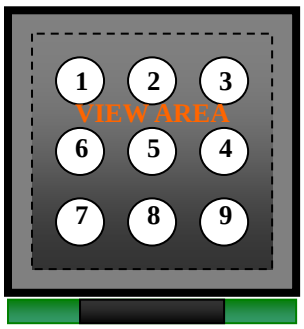
*2：Measurement Condition for Optical Characteristics:

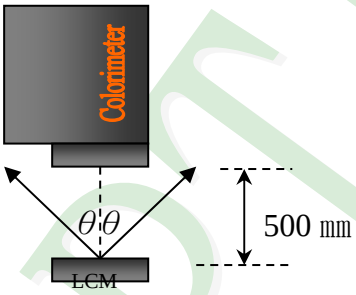
a：Environment: 25℃±5℃ / 60±20%R.H，no wind，dark room below 10 Lux at typical lamp current and typical operating frequency.

b：Measurement Distance: 500 ± 50 ㎜ ，(θ= 0°)

c：Equipment: TOPCON BM-7 fast，(field 1°)，after 10 minutes operation.

d：The uncertainty of the C.I.E coordinate measurement ±0.01，Average Brightness ± 4%





Colorimeter=BM-7 fast

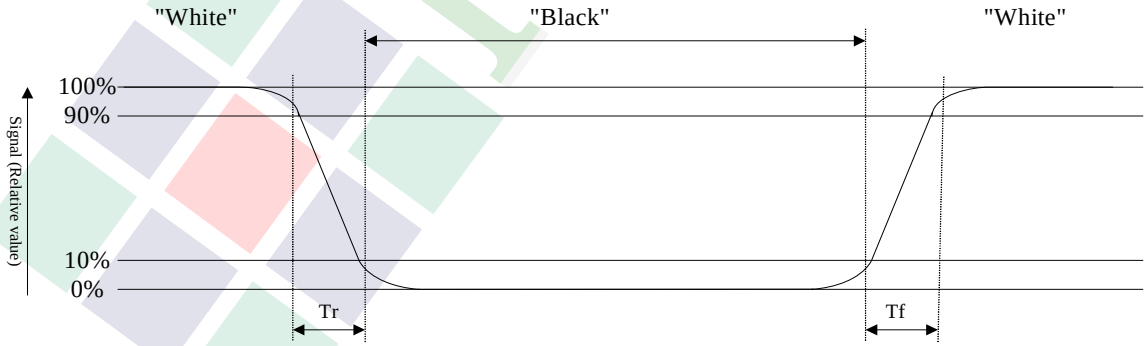
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

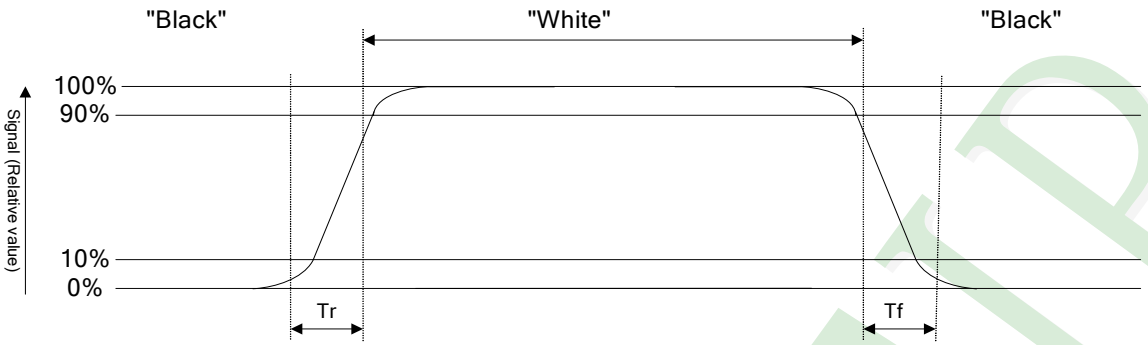
The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White



Normally Black



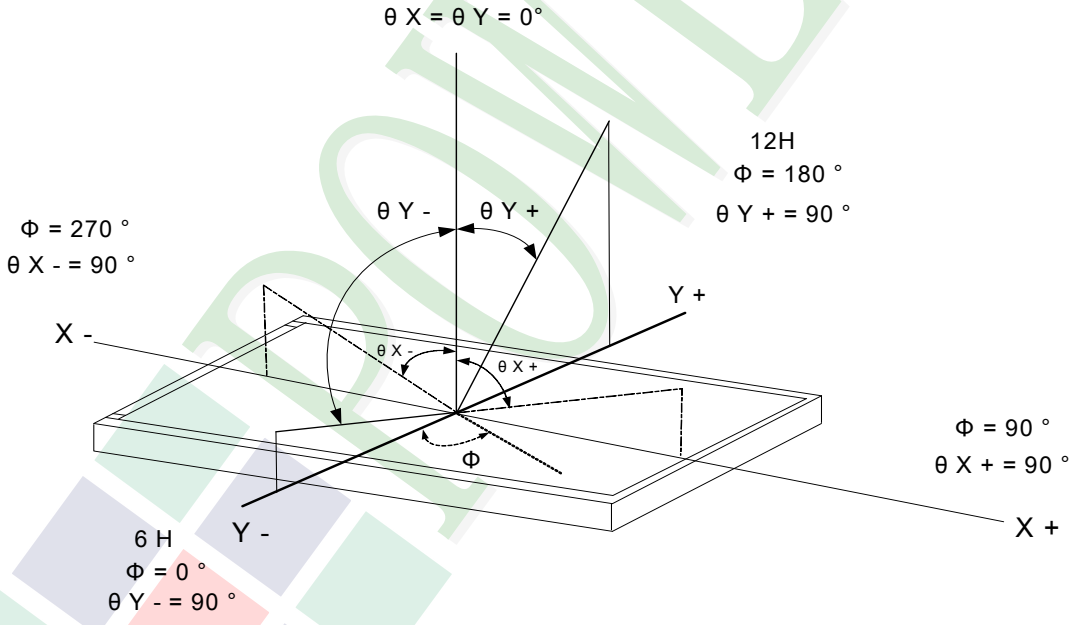
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at “White” state}}{\text{Photo detector output when LCD is at “Black” state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



Note5: Applying with spectrophotometer in the condition of 400 to 700nm, 10nm/each; in accordance with JIS Z 8701 2 degree viewing XYZ system, measuring the reflective rate of 5 degree

1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25℃	-	300	mA
LED Reverse Voltage	VR	Ta =25℃	-	5	V
Power Dissipation	PD	Ta =25℃	-	1.98	W

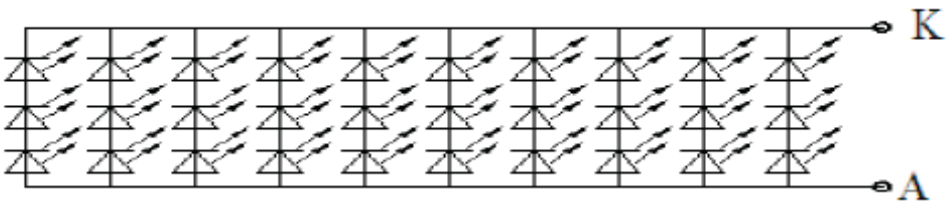
Backlight Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF=200mA	8.1	9.3	10.2	V
Average Brightness (Without LCD)	IV		11200	12800	-	cd/m^2
CIE Color Coordinate (Without LCD)	X		0.25	0.28	0.31	-
	Y		0.28	0.31	0.34	
Uniformity *1	△B		75	-	-	*2
Color	White					

*1：This value will be changed while mass production.

*2：△B=B(min) / B(max)%

B/L Internal Circuit Diagram



Other Description

Item	Conditions	Description
Life Time	Ta =25℃ IF= 200 mA	20000 hrs

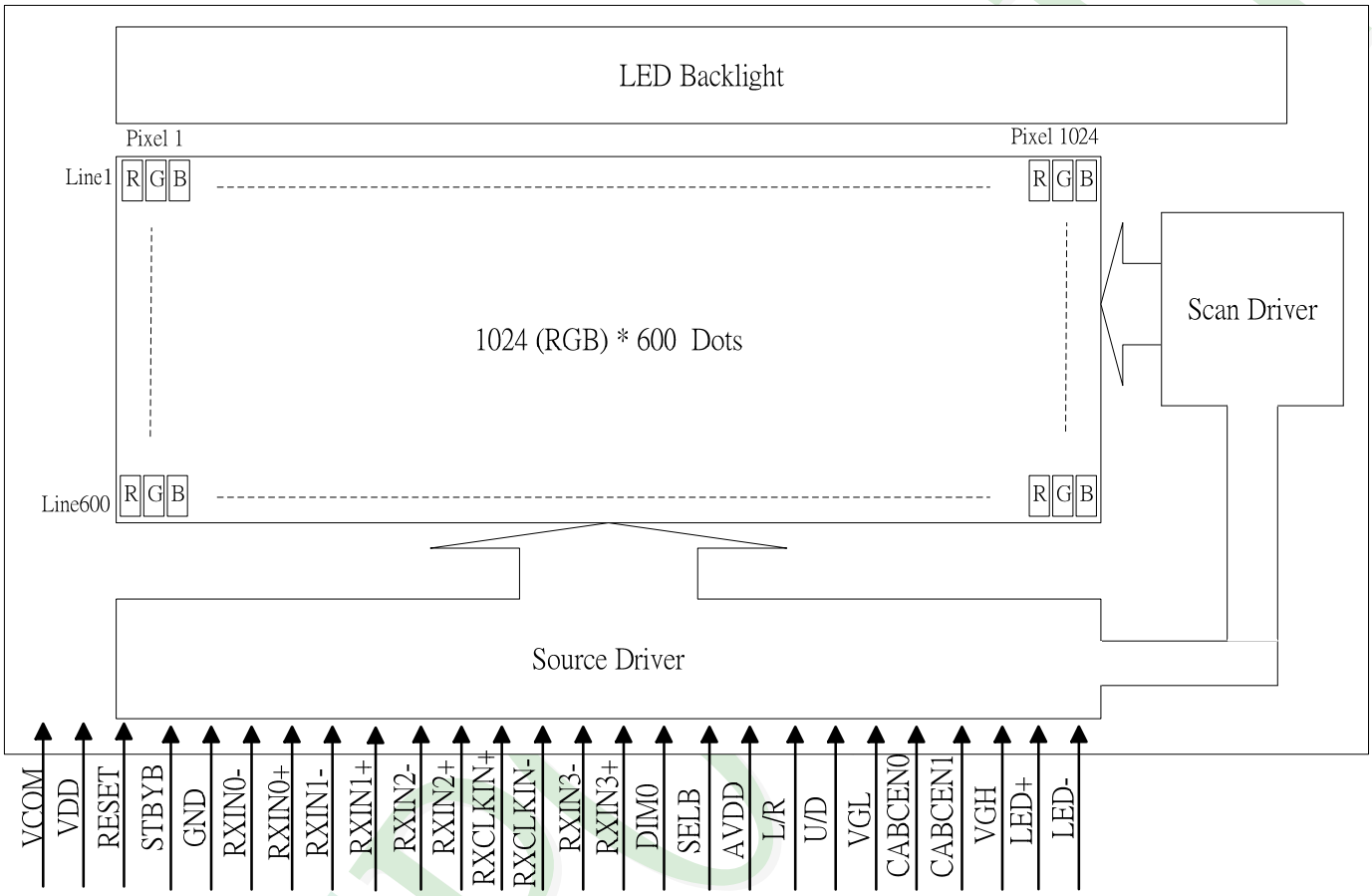
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



2.2 Interface Pin Description

Pin No.	Symbol	Function
1	VCOM	Common Voltage
2	VDD	Power Voltage for digital circuit
3	VDD	Power Voltage for digital circuit
4	NC	No connection
5	Reset	Global reset pin
6	STBYB	Standby mode, Normally pulled high STBYB = “1”, normal operation STBYB = “0”, timing controller, source driver will turn off, all output are High-Z
7	GND	Ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	+ LVDS differential data input
10	GND	Ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	+ LVDS differential data input
13	GND	Ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	+ LVDS differential data input
16	GND	Ground
17	RXCLKIN-	- LVDS differential clock input
18	RXCLKIN+	+ LVDS differential clock input
19	GND	Ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	+ LVDS differential data input
22	GND	Ground
23	NC	No Connection
24	NC	No Connection
25	GND	Ground

Pin No.	Symbol	Function
26	NC	No Connection
27	DIM0	Backlight CABC controller signal output DIMO=L Turn off external backlight controller DIMO=H Logical control signal to turn on external backlight controller
28	SELB	6bit/8bit mode select If LVDS input data is 6 bits ,SELB must be set to High; If LVDS input data is 8 bits ,SELB must be set to Low.
29	AVDD	Power for Analog Circuit
30	GND	Ground
31	LEDK	LED Cathode
32	LEDK	LED Cathode
33	L/R	Horizontal inversion When L/R=”0”, set right to left scan direction. When L/R=”1”, set left to right scan direction.
34	U/D	Vertical inversion When U/D=”0”, set top to bottom scan direction. When U/D=”1”, set bottom to top scan direction.
35	VGL	Gate OFF Voltage
36	CABCEN1	CABC H/W enable Note:1
37	CABCEN0	CABC H/W enable Note:1
38	VGH	Gate ON Voltage
39	LEDA	LED Anode
40	LEDA	LED Anode

Note1:

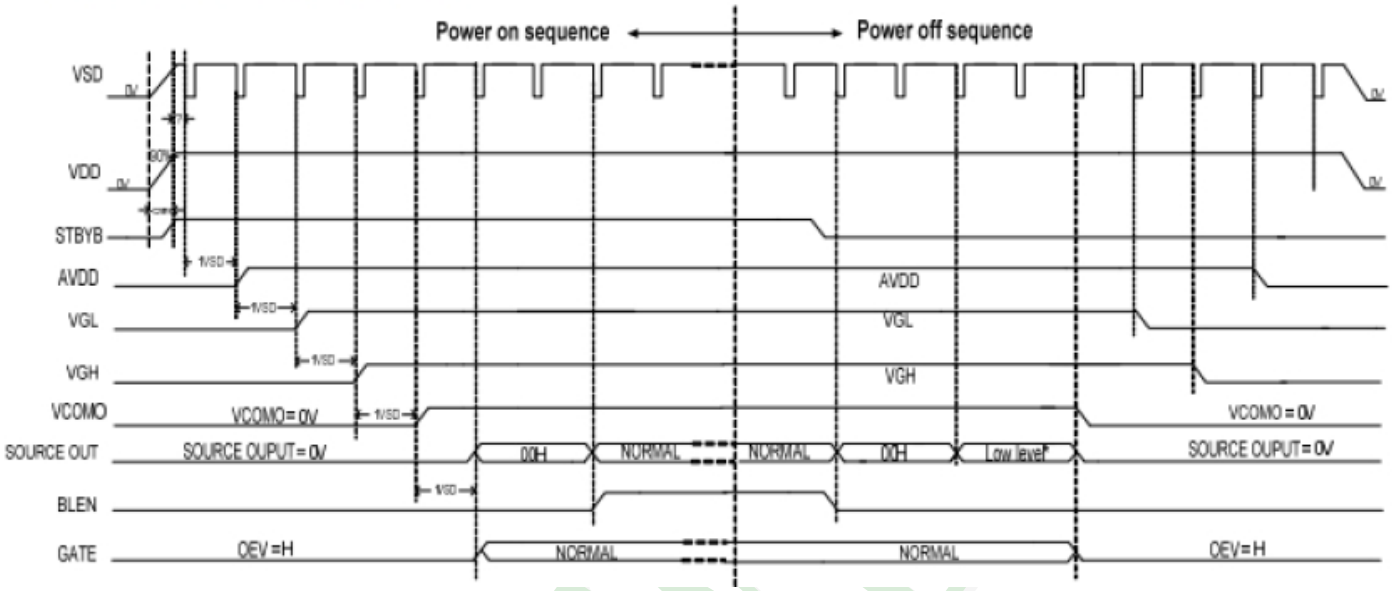
CABCEN1	CABCEN0	DESCRIPTION
L	L	CABC OFF
L	H	User interface Image
H	L	Still Picture
H	H	Moving Image

2.3 Timing Characteristics

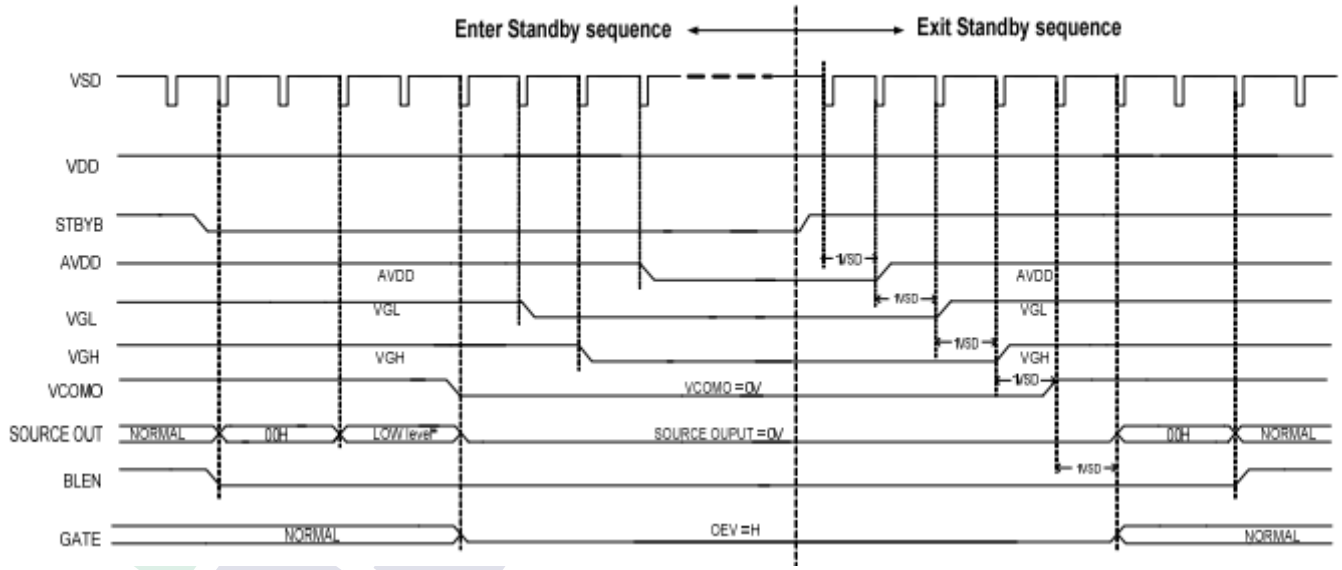
2.3.1 Power Sequence

In order to prevent IC from power on reset fail, the rising time (T_{POR}) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

Power-On/Off Timing Sequence:

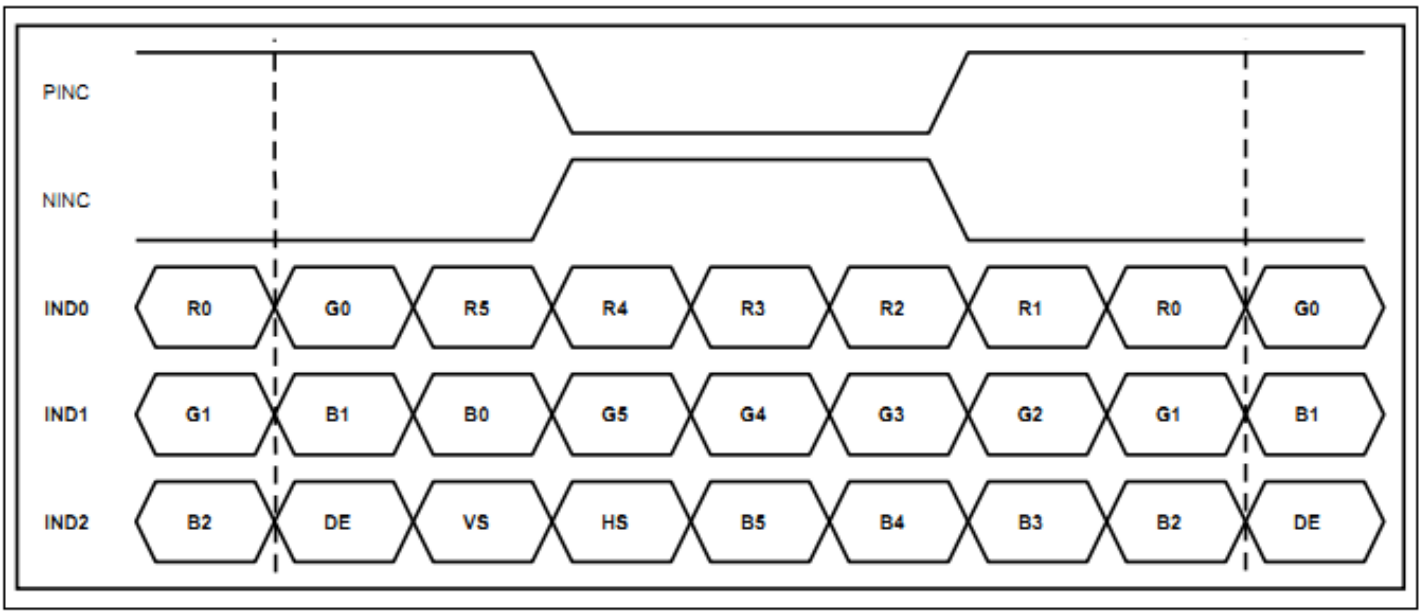


Enter and Exit Standby Mode Sequence:

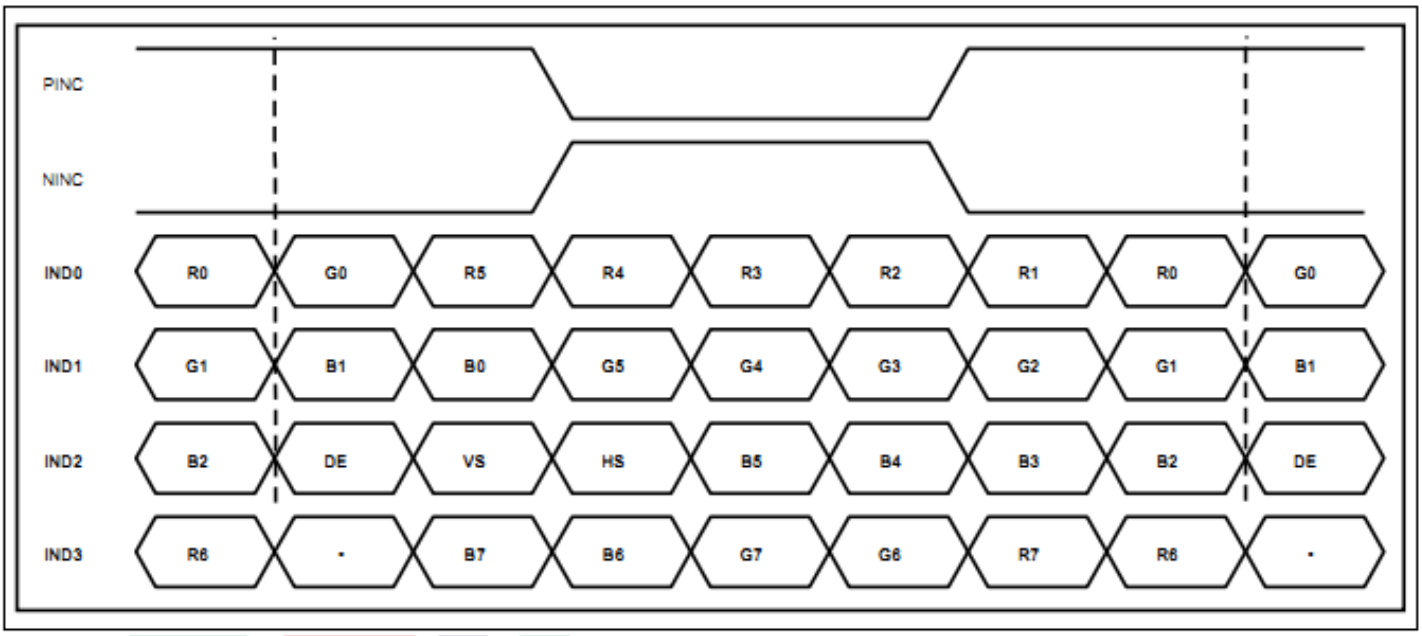


2.3.2 Data Input Format for LVDS

6bit LVDS input



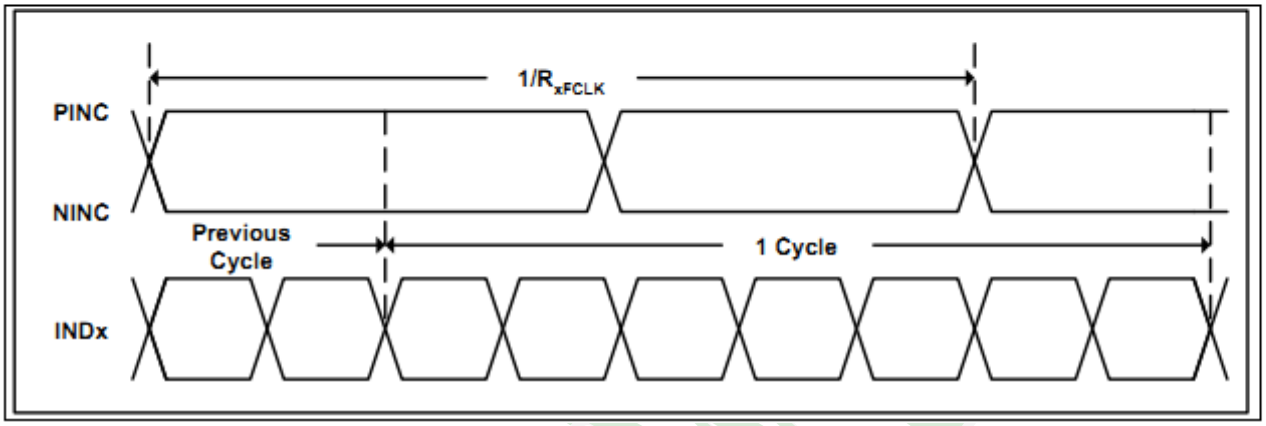
8-bit LVDS input (HSD='L')



2.4 AC Electrical Characteristics

LVDS mode

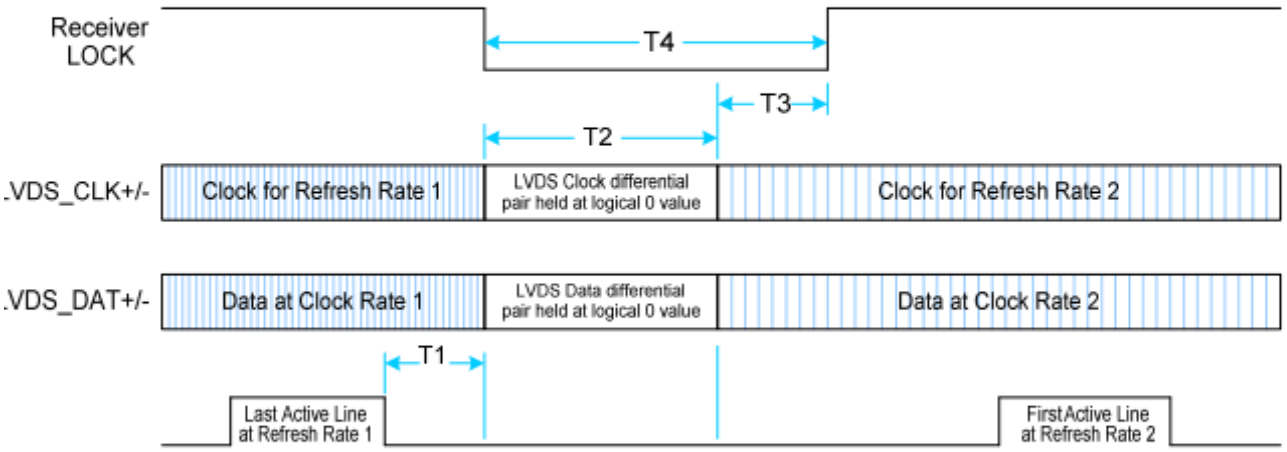
Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Clock frequency	R_{xFCLK}	20		71	MHz	
Input data skew margin	T_{RSKM}	500			pS	$\|V_{ID}\|$ = 400mV R_{xVCM} = 1.2V R_{xFCLK} = 71 MHz
Clock high time	T_{LVCH}		4/(7* R_{xFCLK})		ns	
Clock low time	T_{LVCL}		3/(7* R_{xFCLK})		ns	
PLL wake-up time	T_{enPLL}			150	uS	



2.5 Timing

SDRRS (seamless display refresh rate switching)

When Showing the still picture, it is accept to reduce the refresh rate from 60Hz to low refresh rate (for example 40Hz). The purpose is mainly for power saving. INTEL defined a timing chart switch between different refresh rate. Following this timing chart, the switch between different refresh rates is seamless for end user.



T1 - Min delay from start of vert blank to start of timing change: 2 lines (HSYNC periods)

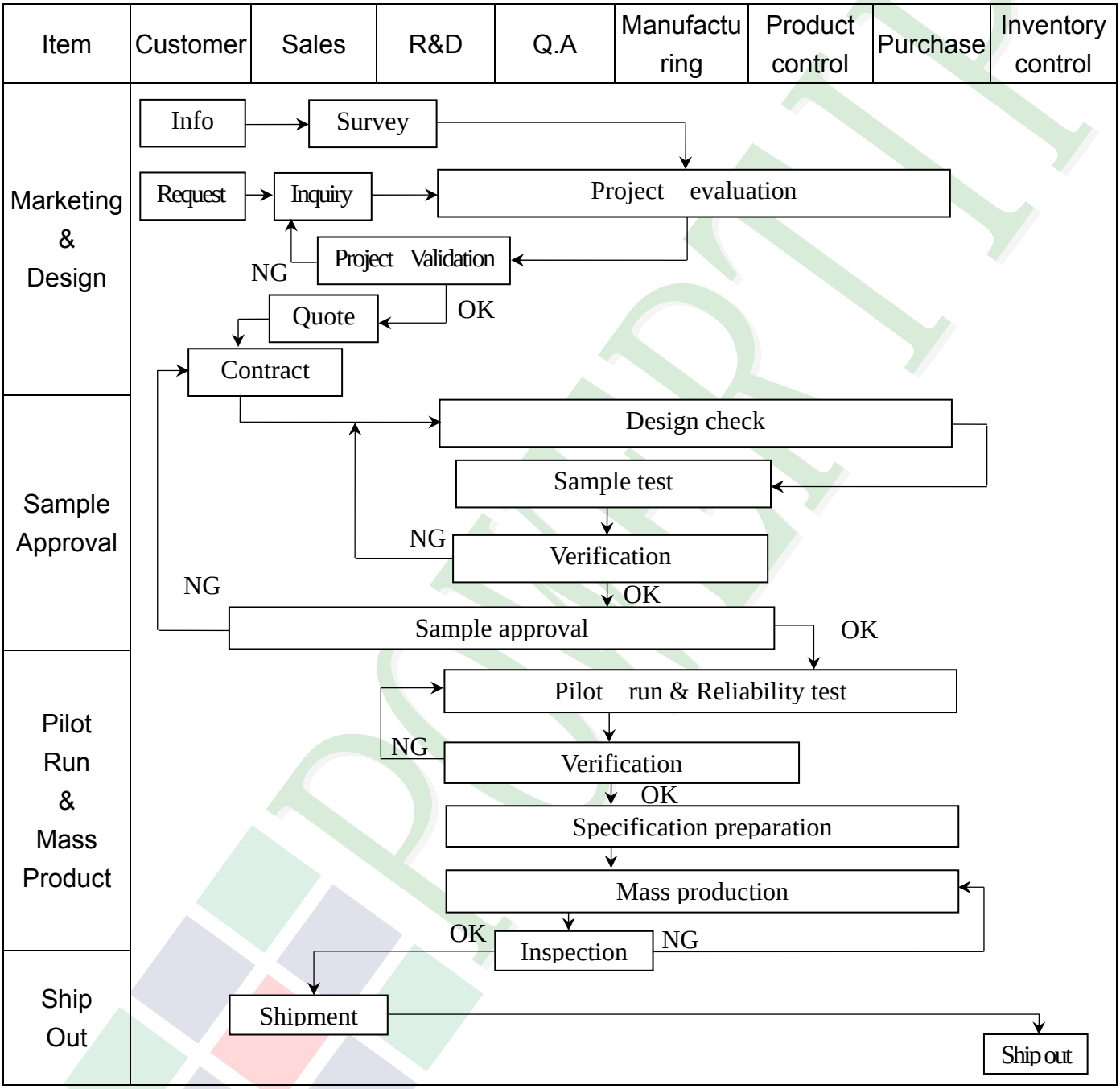
T2 - Max delay for clock to transition to new frequency: 100us

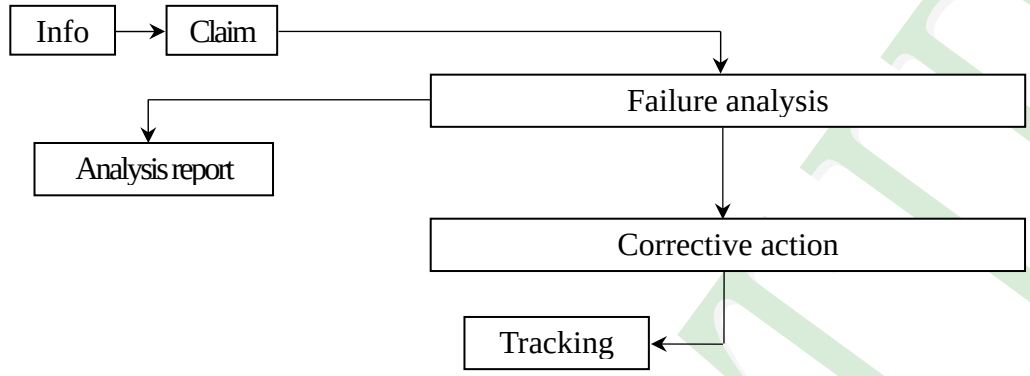
T3 - Max receiver lock delay from stable clock: Display specific (TBD)

T4 - Max period during which panel maintains display (T2+T3): Display specific (TBD)

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2. Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5" -15" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.

◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

◆OUT Going Defect Level: Sampling.

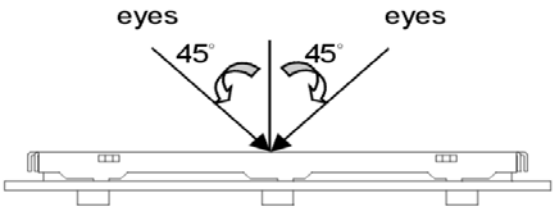
◆Standard of the product appearance test:

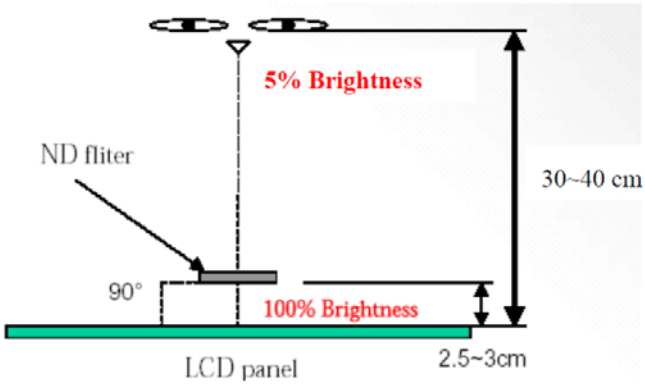
a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)

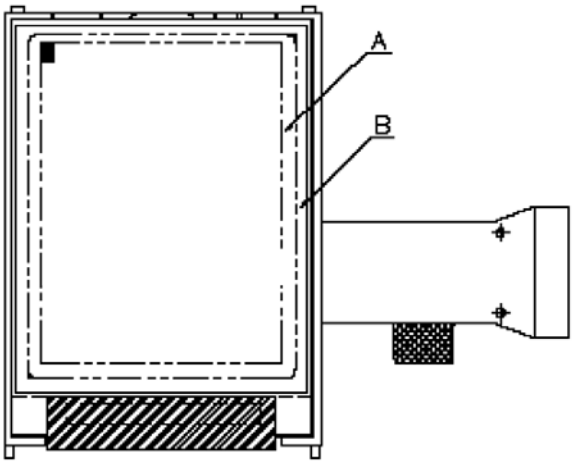
, and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.





(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection： (Unit：mm)

◆Specification For TFT-LCD Module 3.5" ~15" : (Ver.B01)

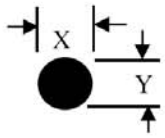
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="3">01</td><td rowspan="3">Product condition</td><td>1.1The part number is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>1.2 Mixed product types.</td><td>Major</td></tr>
<tr><td>1.3 Assembled in inverse direction.</td><td>Major</td></tr>
<tr><td>02</td><td>Quantity</td><td>2.1The quantity is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>03</td><td>Outline dimension</td><td>3.1Product dimension and structure must conform to structure diagram.</td><td>Major</td></tr>
<tr><td rowspan="6">04</td><td rowspan="6">Electrical Testing</td><td>4.1 Missing line character and icon.</td><td>Major</td></tr>
<tr><td>4.2 No function or no display.</td><td>Major</td></tr>
<tr><td>4.3 Display malfunction.</td><td>Major</td></tr>
<tr><td>4.4 LCD viewing angle defect.</td><td>Major</td></tr>
<tr><td>4.5 Current consumption exceeds product specifications.</td><td>Major</td></tr>
<tr><td>4.6Mura cannot be seen through 5% ND filter at 50% Gray , should be judged by the viewing angle of 90 degree.</td><td>Minor</td></tr>
<tr><td>05</td><td>Dot defect

(Bright dot, Dark dot)

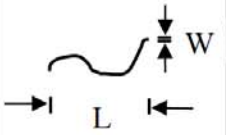
On -display</td><td>
<table>
<tr><th colspan="2">Item</th><th>Acceptance (Q'ty)</th></tr>
<tr><td rowspan="4">Dot Defect</td><td>Bright Dot</td><td>≦ 4</td></tr>
<tr><td>Dark Dot</td><td>≦ 5</td></tr>
<tr><td>Joint Dot</td><td>≦ 3</td></tr>
<tr><td>Total</td><td>≦ 7</td></tr>
</table>
5.1 Inspection pattern: full white, full black, Red, Green and blue screens.
5.2 It is defined as dot defect if defect area ＞1/2 dot.
5.3 The distance between two dot defect ≧5 mm.
5.4 Bright dot that can not be seen through 5% ND filter.
</td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

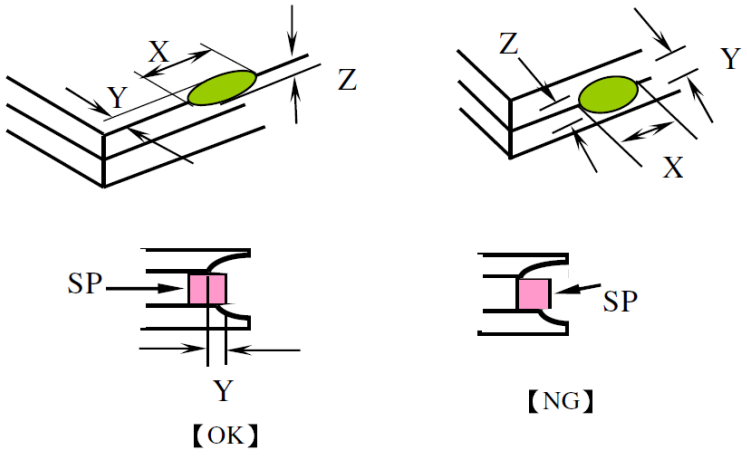
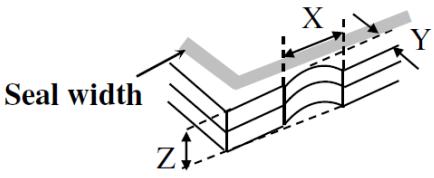
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>06</td><td>Black or white Dot, scratch, contamination

Round type
 

Φ=(x+y) / 2

Line type
 
</td><td>6. 1 Round type (Non-display or display):
<table><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≦ 0.25</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>0.25 < Φ ≦ 0.50</td><td>5</td></tr><tr><td>Φ > 0.50</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table>
6. 2 Line type(Non-display or display):
<table><tr><th rowspan="2">module size</th><th rowspan="2">Length (L)</th><th rowspan="2">Width (W)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td rowspan="5">3.5" to less 9"</td><td>---</td><td>W ≦ 0.03</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>L ≦10.0</td><td>0.03 <W ≦ 0.05</td><td>4</td></tr><tr><td>L ≦5.0</td><td>0.05 <W ≦ 0.10</td><td>2</td></tr><tr><td>---</td><td>W >0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr><tr><td rowspan="4">9" to 15"</td><td>---</td><td>W ≦ 0.05</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>L ≦10.0</td><td>0.05 <W ≦ 0.10</td><td>5</td></tr><tr><td>---</td><td>W >0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr></table></td><td>Minor</td></tr>
<tr><td>07</td><td>Polarizer Bubble</td><td><table><tr><th rowspan="2">Dimension (diameter: Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≦ 0.25</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>0.25 < Φ ≦ 0.50</td><td>4</td></tr><tr><td>0.50 < Φ ≦ 0.80</td><td>1</td></tr><tr><td>Φ >0.80</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table></td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

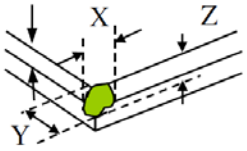
NO	Item	Criterion	Level
08	The crack of glass	Symbols : X: The length of crack Y: The width of crack. Z: The thickness of crack W: terminal length t: The thickness of glass a: LCD side length 8.1 General glass chip: 8.1.1 Chip on panel surface and crack between panels:  【OK】 【NG】  	Minor

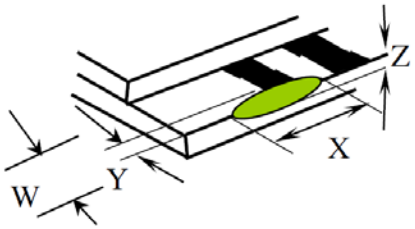
X	Y	Z
≦ a	Crack can't enter viewing area	≦1/2 t
≦ a	Crack can't exceed the half of SP width.	1/2 t < Z ≦2 t

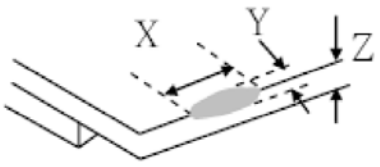
◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

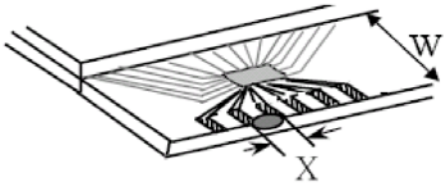
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="2">08</td><td rowspan="2">The crack of glass</td><td>Symbols :

X: The length of crack Y: The width of crack.
Z: The thickness of crack W: terminal length
t: The thickness of glass a: LCD side length

8.1.2 Corner crack:
 

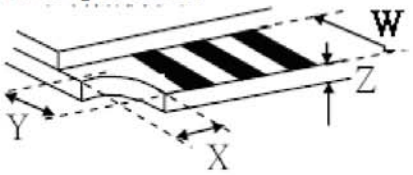
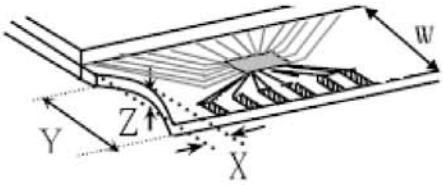
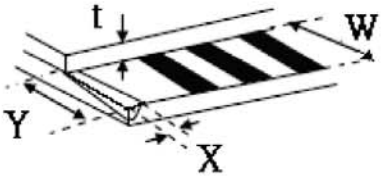
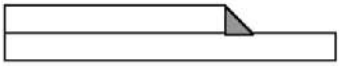
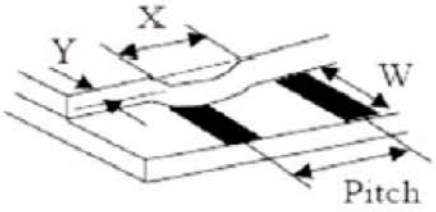
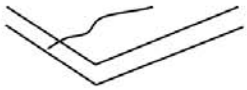
<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/5 a</td><td>Crack can’t enter viewing area</td><td>Z ≦ 1/2 t</td></tr>
<tr><td>≦1/5 a</td><td>Crack can’t exceed the half of SP width.</td><td>1/2 t < Z ≦ 2 t</td></tr>
</table></td><td rowspan="2">Minor</td></tr>
<tr><td>8.2 Protrusion over terminal:
8.2.1 Chip on electrode pad:
 





<table>
<tr><th></th><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>Front</td><td>≦ a</td><td>≦ 1/2 W</td><td>≦ t</td></tr>
<tr><td>Back</td><td>≦ a</td><td>≦ W</td><td>≦ 1/2 t</td></tr>
</table></td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

NO	Item	Criterion	Level
08	The crack of glass	Symbols: X: The length of crack Y: The width of crack. Z: The thickness of crack W: terminal length t: The thickness of glass a: LCD side length 8.2.2 Non-conductive portion:    X: ≦ 1/3 a; Y: ≦ W; Z: ≦t ⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8.2.3 Glass remain:   X: ≦ a; Y: ≦ 1/3 W; Z: ≦t 8.2.4 Cracking:  Not Allowed	Minor

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1Pin type、quantity、dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
		10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≦1.5 mm.	Minor

4. RELIABILITY TEST

(Ver.B01)

<table>
<tr><th>NO.</th><th>TEST ITEM</th><th>TEST CONDITION</th></tr>
<tr><td>1</td><td>High Temperature Storage Test</td><td>Keep in 80 ±5℃ 240 hrs</td></tr>
<tr><td>2</td><td>Low Temperature Storage Test</td><td>Keep in -30 ±5℃ 240 hrs</td></tr>
<tr><td>3</td><td>High Temperature / High Humidity Storage Test</td><td>Keep in 60 ℃ / 90% R.H duration for 240 hrs
(Excluding the polarizer)</td></tr>
<tr><td>4</td><td>Temperature Cycling Storage Test</td><td>-30℃ → +25℃ → 80℃ → +25℃
(30mins) (5mins) (30mins) (5mins)
20 Cycle</td></tr>
<tr><td rowspan="2">5</td><td rowspan="2">ESD Test</td><td>Air Discharge:
Apply 2 KV with 5 times
Discharge for each polarity +/-
Contact Discharge:
Apply 250 V with 5 times
discharge for each polarity +/-</td></tr>
<tr><td>1. Temperature ambiance : 15℃～35℃
2. Humidity relative : 30%～60%
3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%
4. Discharge Resistance(Rd) : 330Ω±10%
5. Discharge, mode of operation :
Single Discharge (time between successive discharges at least 1 sec)
(Tolerance if the output voltage indication : ±5%)</td></tr>
<tr><td>6</td><td>Vibration Test (Packaged)</td><td>1. Sine wave 10～55 Hz frequency (1 min/sweep)
2. The amplitude of vibration :1.5 mm
3. Each direction (X、Y、Z) duration for 2 Hrs</td></tr>
<tr><td>7</td><td>Drop Test (Packaged)</td><td><table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table>Drop Direction :※1 corner / 3 edges / 6 sides each 1time</td></tr>
</table>

◎Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function.

(Normal operation state)

Temperature：+20~30℃

Humidity：50~70%

Atmospheric pressure：86~106Kpa

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands, this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 320 ± 10°C and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM

5.2.10 Caution!(LCM products with Capacitive Touch Panel)
Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
Therefore, the touch needs to be thoroughly tested inside the target application.

5.2.11 Do not let the LCD screen display static images (text, logos or pictures) for a prolonged period of time to prevent possible image burn-in.

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.

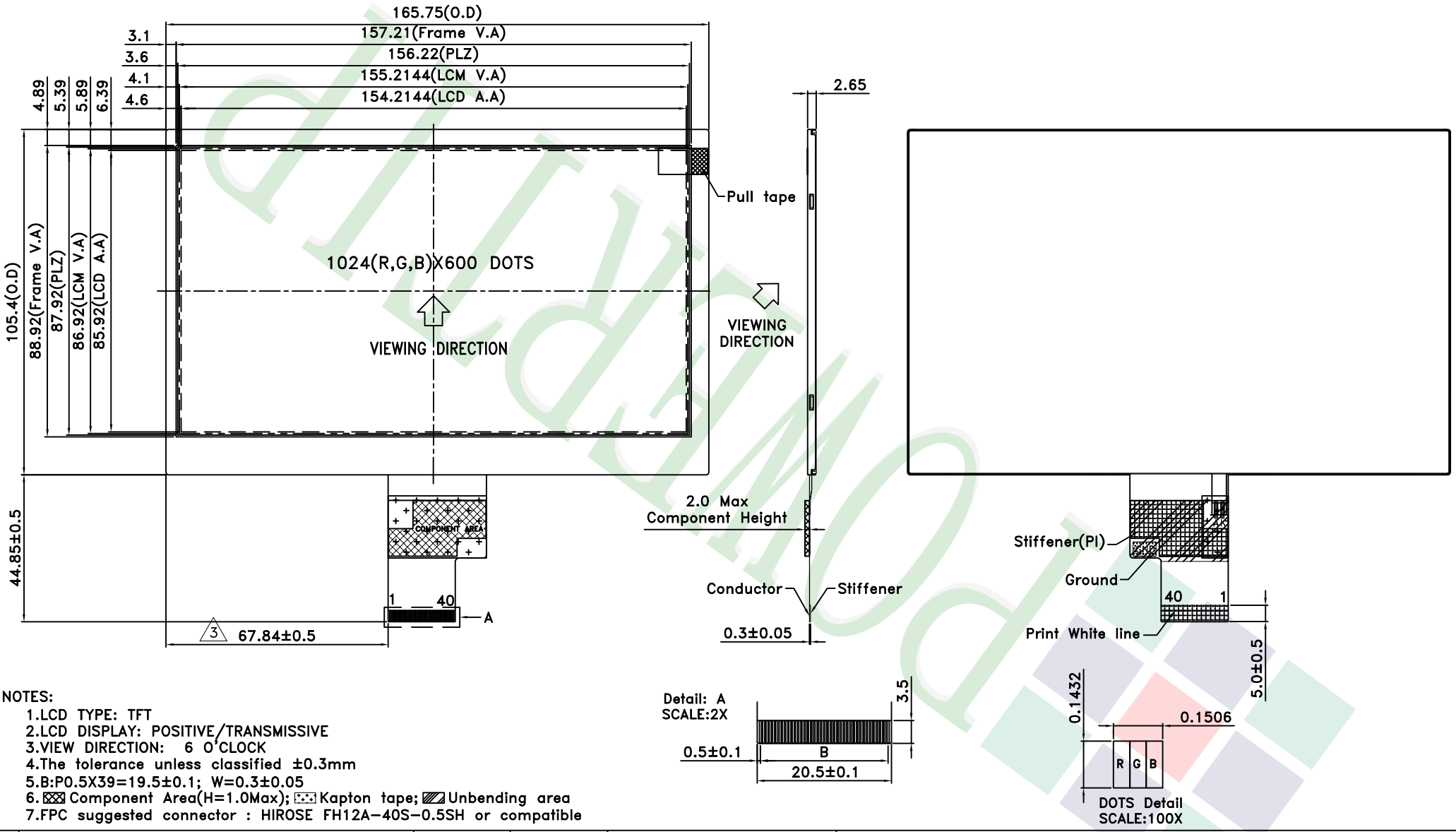
5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



NOTES:

1.LCD TYPE: TFT
2.LCD DISPLAY: POSITIVE/TRANSMISSIVE
3.VIEW DIRECTION: 6 O'CLOCK
4.The tolerance unless classified ±0.3mm
5.B:P0.5X39=19.5±0.1; W=0.3±0.05
6. Component Area(H=1.0Max); Kapton tape; Unbending area
7.FPC suggested connector : HIROSE FH12A-40S-0.5SH or compatible

REV	REV BY	REVISER	DATE
007			
006			
005			
004			
003	Modify Dimension	Clare	2020/5/13
002	Modify FPC Design	Clare	2020/3/10
001	NEW DRAWING	Clare	2020/1/20

PART NO: PH102600T015-IBA

DRAWING NAME : LMD-PH102600T015-IBA

TITLE: LCD MODULE DRAWING

久正光電股份有限公司
POWERTIP TECHNOLOGY CORPORATION

Design	Clare Chen	(3)	Surface		
Check	Tina Chen	Unit	MM	Material	
		Scale	1:1	Thickness	
Approve	Jimmy Chen	Page	1/1	Quantity	

Length(Min) (mm) \ Tolerance \ Precision	Precision Level
1 ~ 4	-
4 ~ 16	-
16 ~ 63	-
63 ~ 250	-
250 ~ 1000	-

Ver.001	
Documents NO.	PKG-PH102600T015-IBA

LCM包裝規格書
LCM Packaging Specifications

Approve	Check	Contact
Rex	Tina	Clare

1.包裝材料規格表 (Packaging Material) : (per carton)

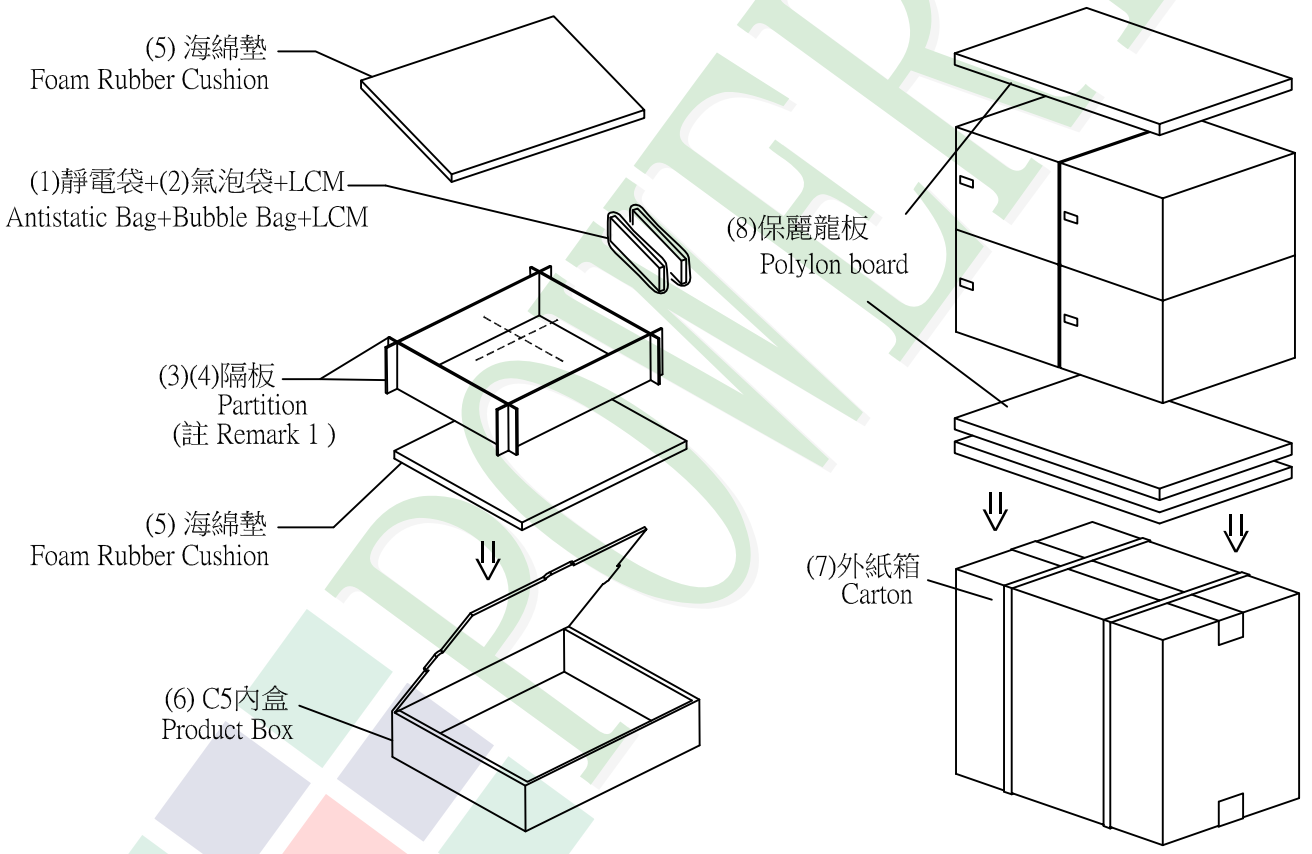
No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH102600T015-IBA	165.75 X 105.4	0.0989	120	11.868
2	靜電袋(1)Antistatic Bag	BAG240170ARABA	240 X 170	0.0048	120	0.576
3	氣泡袋(2)Bubble Bag	BAG170150BRABA	170 X 150	0.0045	60	0.27
4	A9隔板(3)A9 Partition	BX00000000058	245 X 125 X 4	0.0204	64	1.3056
5	B9隔板(4)B9 Partition	BX00000000057	295 X 125 X 4	0.0209	8	0.1672
6	海綿墊(5)Foam Rubber Cushion	OTFOAM00006ABA	290 X 240 X 10	0.02	8	0.16
7	C5內盒(6)Product Box	BX00000000059	310 X 255 X 155	0.248	4	0.992
8	外紙箱(7)Carton	BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
9	保麗龍板(8)Polylon board	OTPLB00000017	510 X 310 X 15	0.025	3	0.075

2.一 整箱總重量 (Total LCD Weight in carton) : 16.24 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

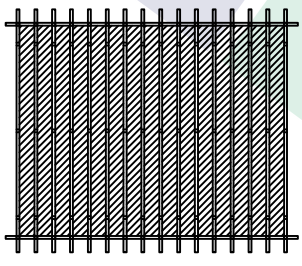
(1)Quantity Of Spacer : A9隔板 X 16 , B9隔板 X 2

(2)Total LCM quantity in carton : quantity per box 30 x no of boxes 4 = 120



特 記 事 項 (REMARK)

4. LCM排放示意圖(前後間隔不放置):
4. LCM placed as figure showing:
(First and last slot should be empty)



模組(LCM) X 2pcs.

模組以靜電袋包妥，FPC反折再模組背面
2片用靜電袋包好后模組放入1個氣泡袋